

This is the page title - Bits  
This is a rung comment.

This program is totally useless...

as far as controlling any process. Now on the other hand if you would like to see how different instructions are converted read on.

This program was converted using the DHRIO option. The racks get converted to full racks on a universal Remote I/O. The PLC2 does not save the rack information so there is no way to know what hardware actually exists. Bits in the rack range will be remapped to I/O. Bits outside of this range will point to the INT array.

I/O bit addresses use the format: \_RRS:I.Data[M].B

where:

- RR = rack in octal
- S = Starting module group
- I = Input (or O for output)
- M = Module group offset
- B = Terminal number in decimal

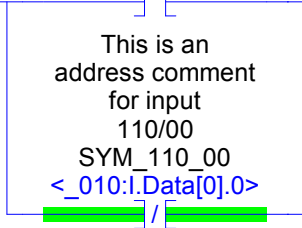
This section of rungs show how bits are converted.

This is an address comment for input  
110/00  
SYM\_110\_00  
<\_010:I.Data[0].0>

This is an address comment for output  
010/00  
SYM\_010\_00  
<\_010:O.Data[0].0>

0

This is an address comment for input  
110/00  
SYM\_110\_00  
<\_010:I.Data[0].0>



Comment for 110/01  
SYM\_110\_01  
<\_010:I.Data[0].1>

Comment for 010/01  
SYM\_010\_01  
<\_010:O.Data[0].1>

1

Comment for 110/02  
SYM\_110\_02  
<\_010:I.Data[0].2>

Description for 010/02  
SYM\_010\_02  
<\_010:O.Data[0].2>

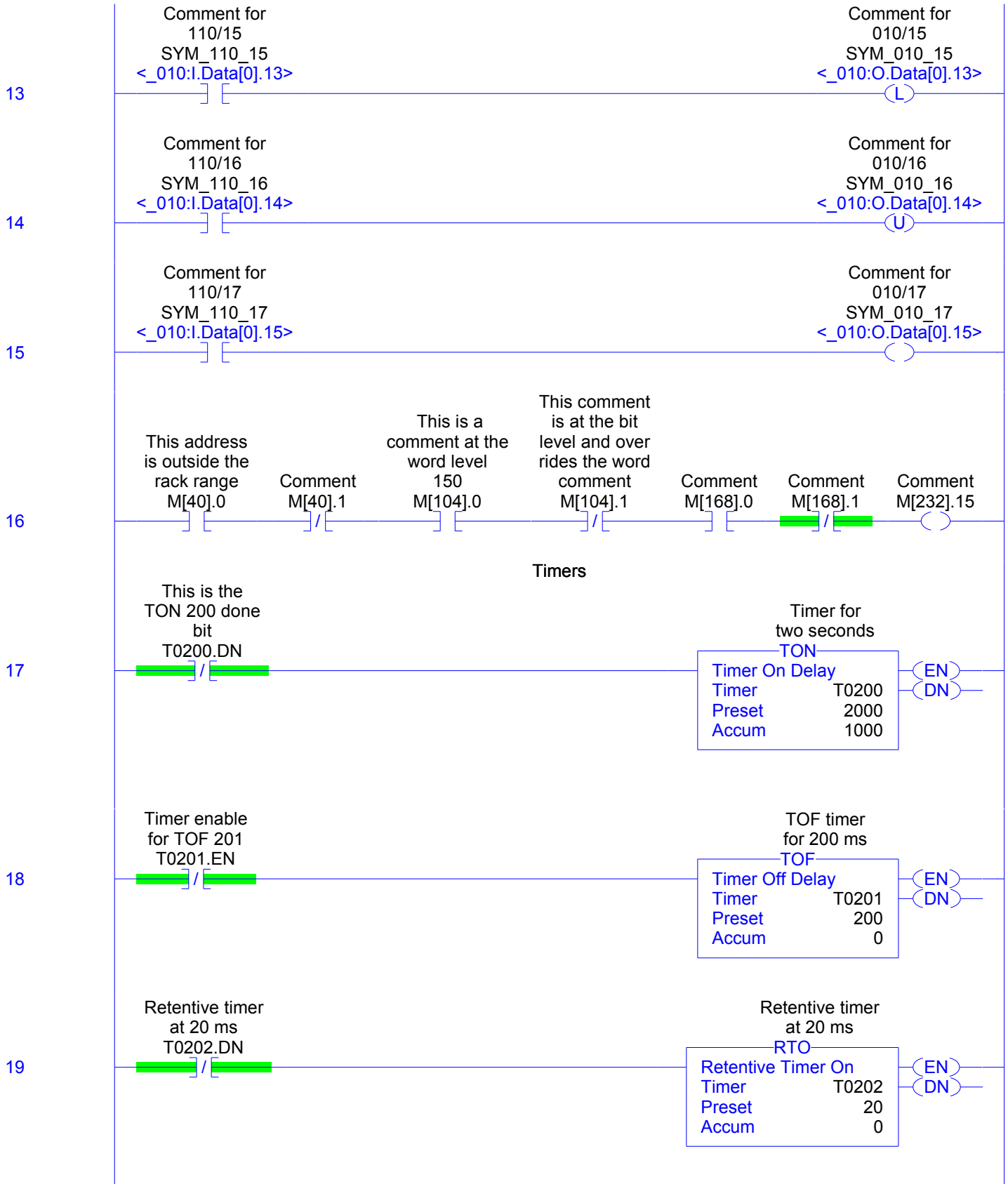
2

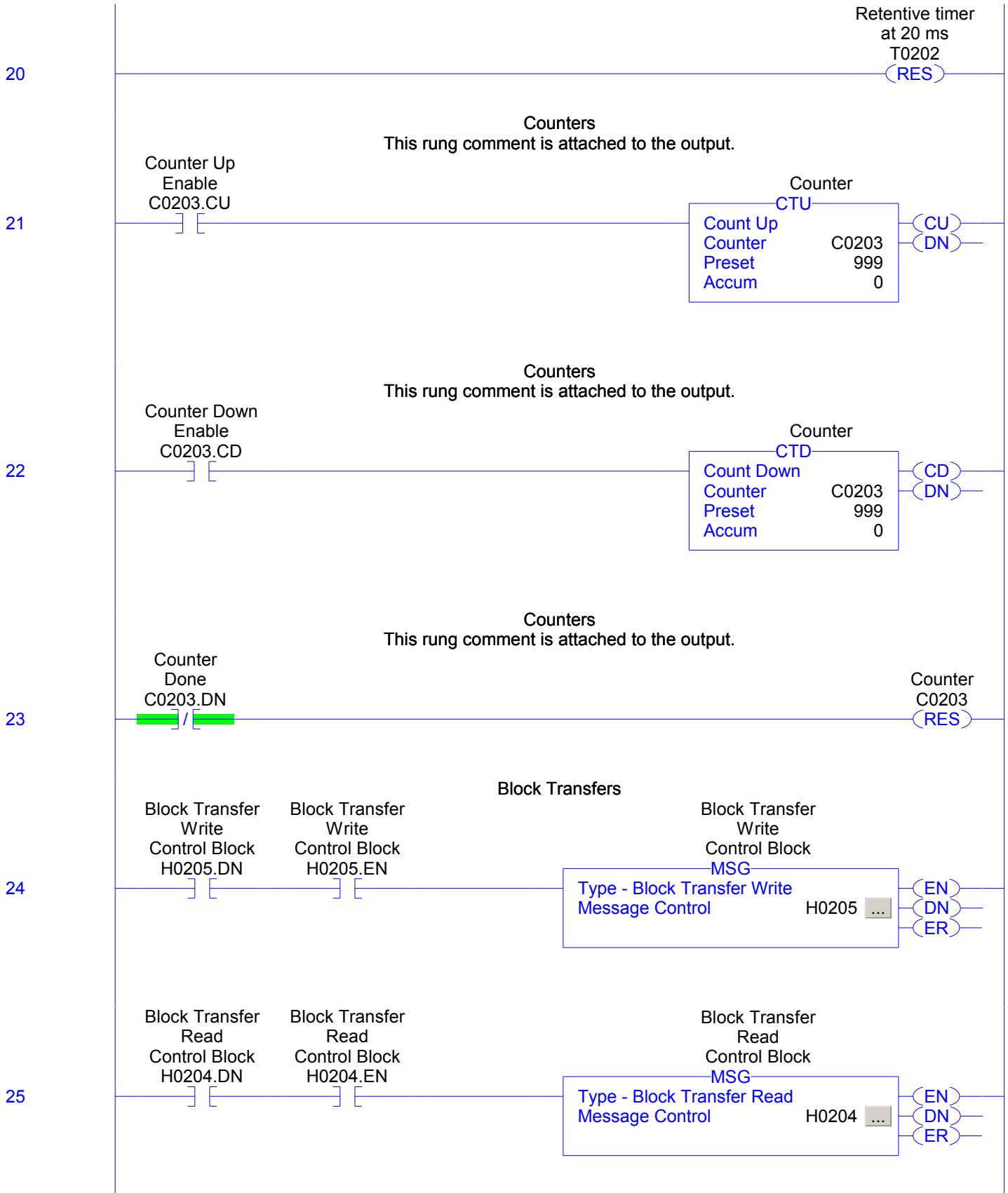
Comment for 110/03  
SYM\_110\_03  
<\_010:I.Data[0].3>

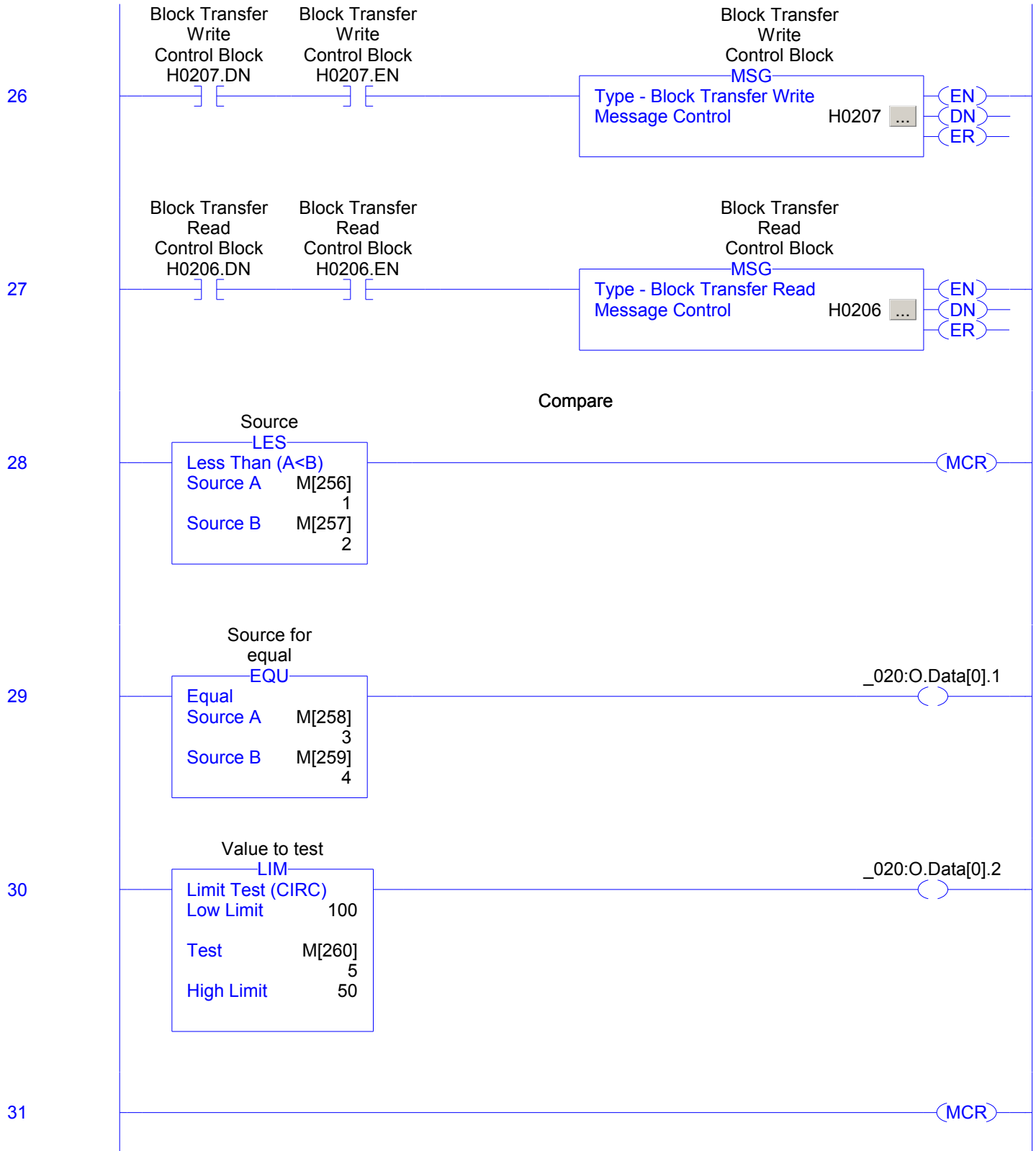
Comment for 010/03  
SYM\_010\_03  
<\_010:O.Data[0].3>

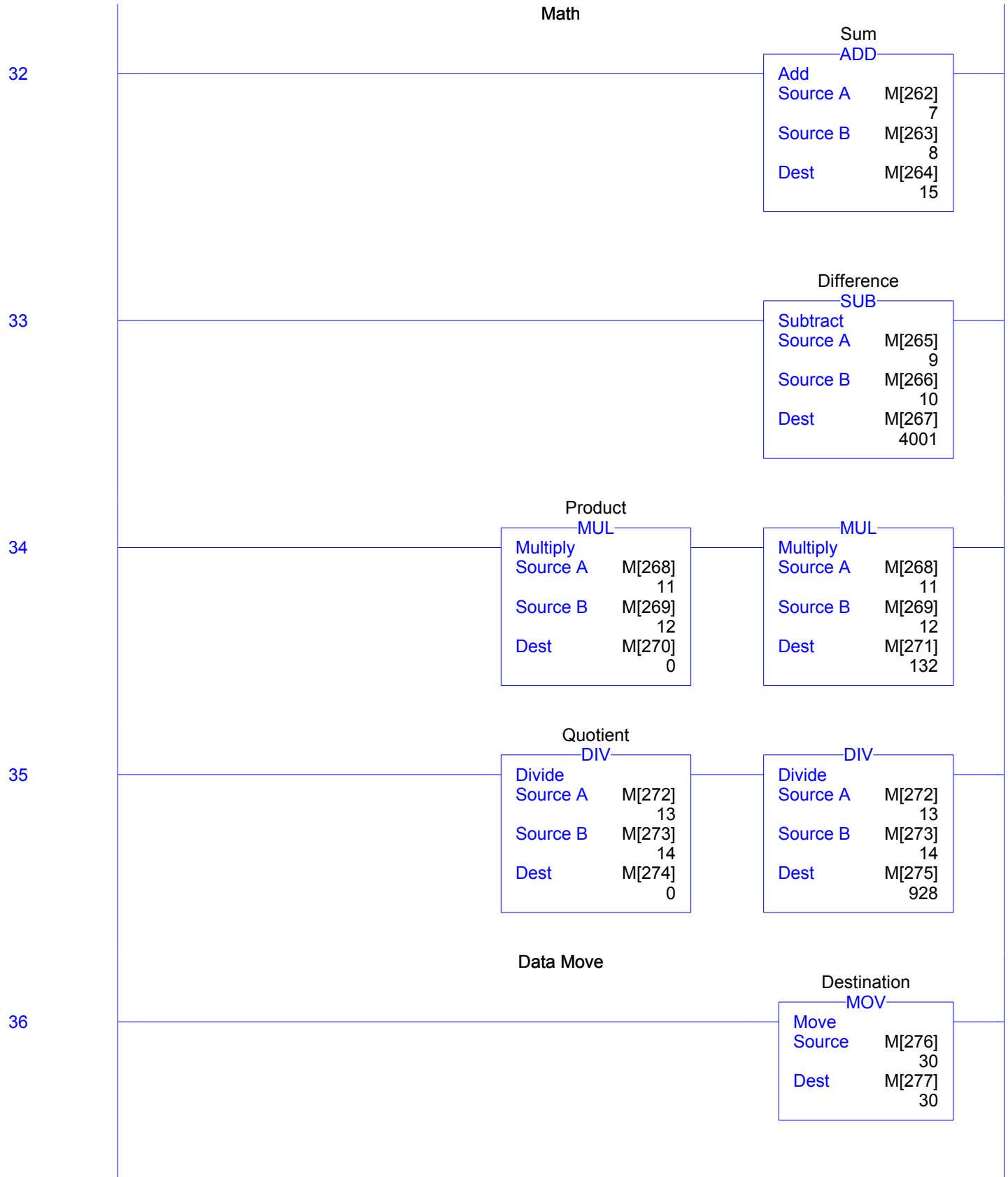
3

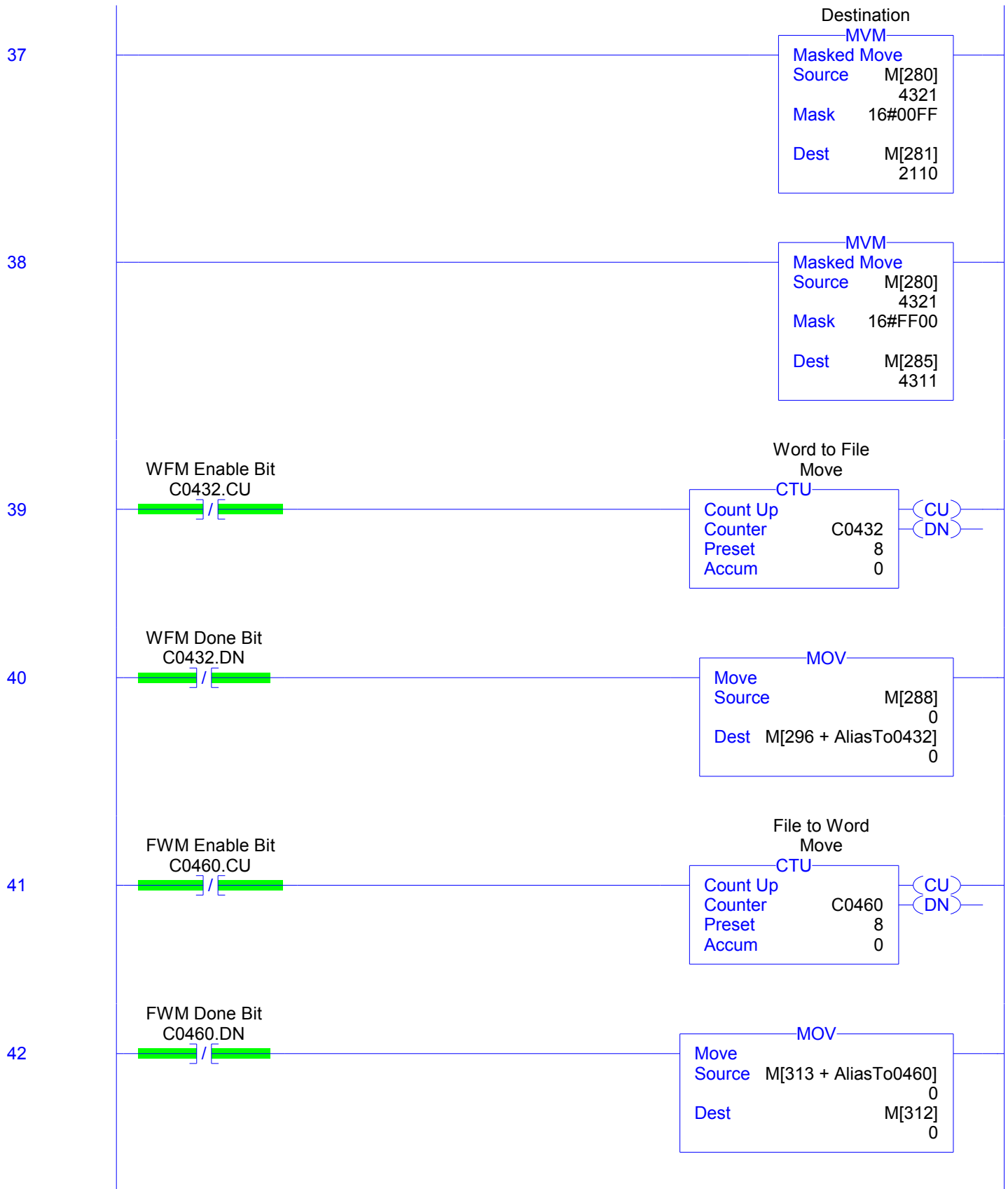


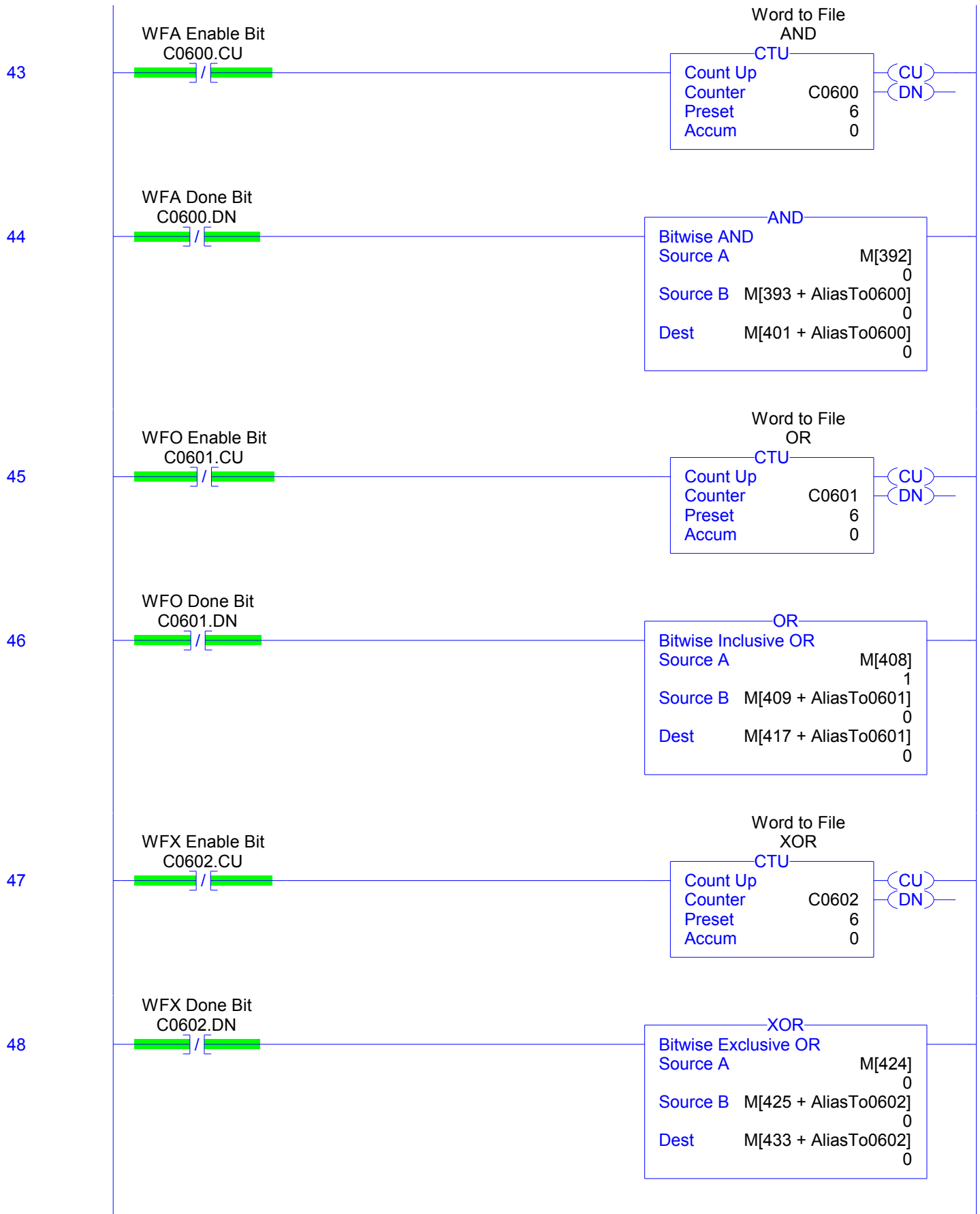




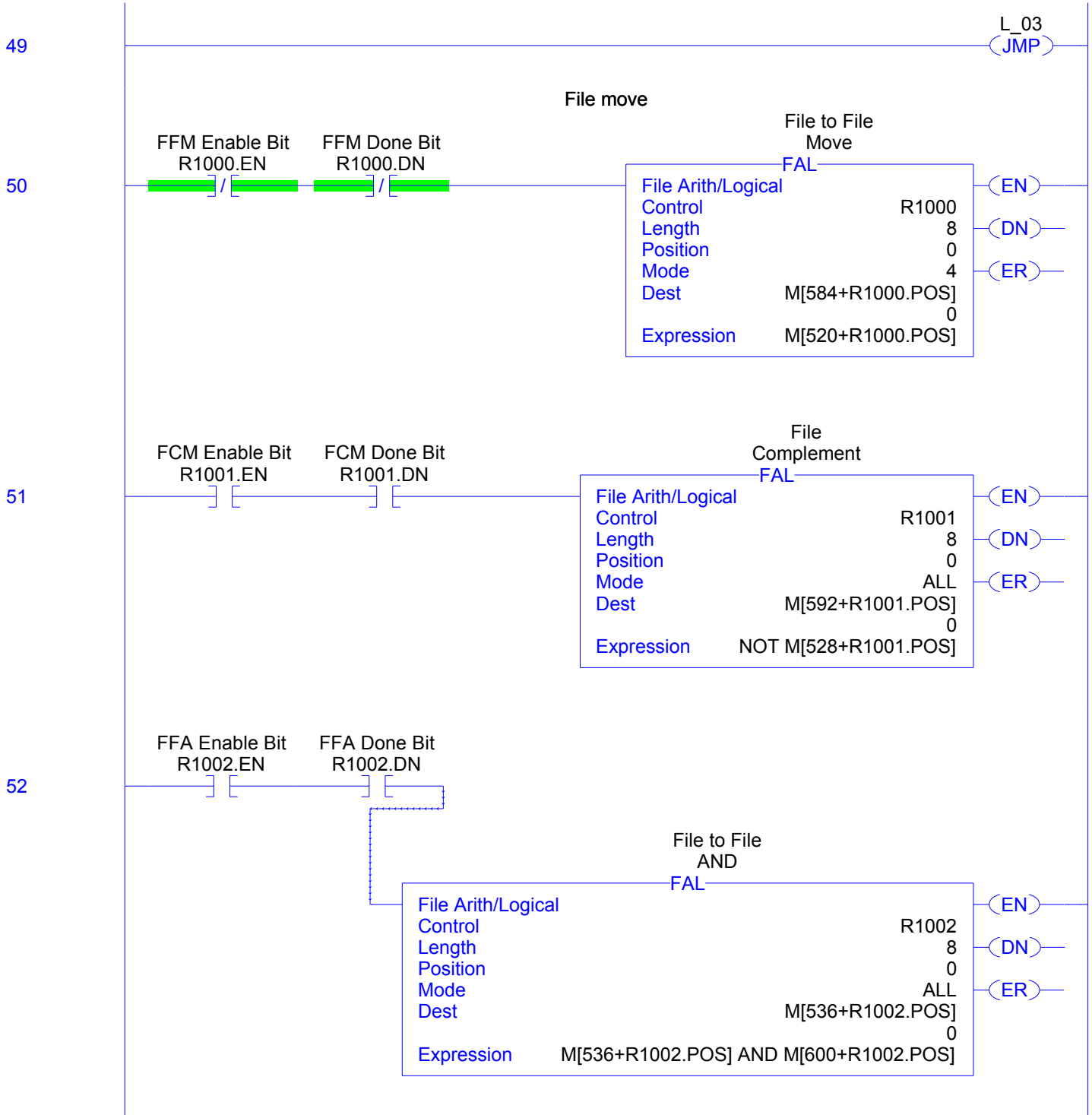


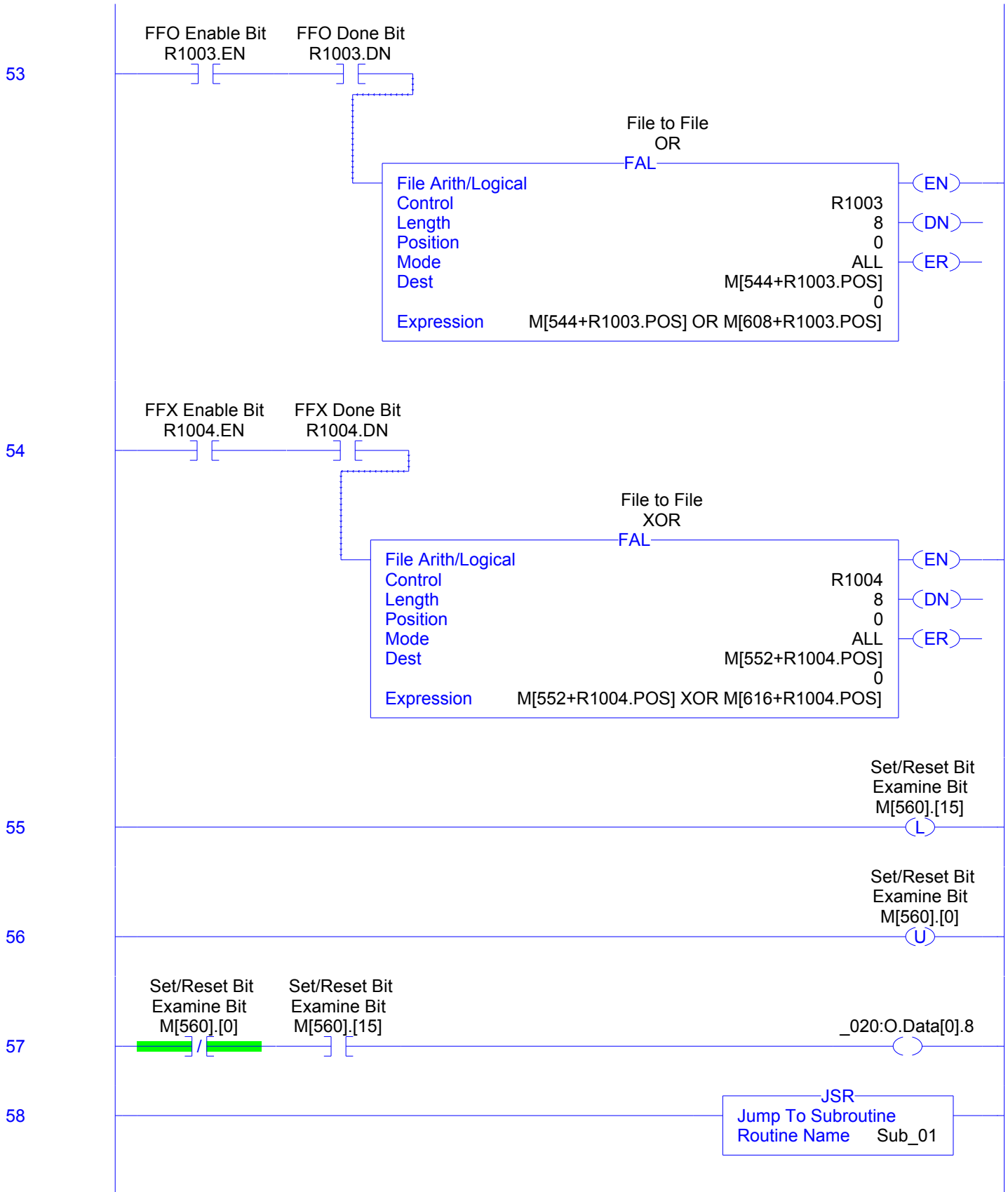












Shift Sequence

59

