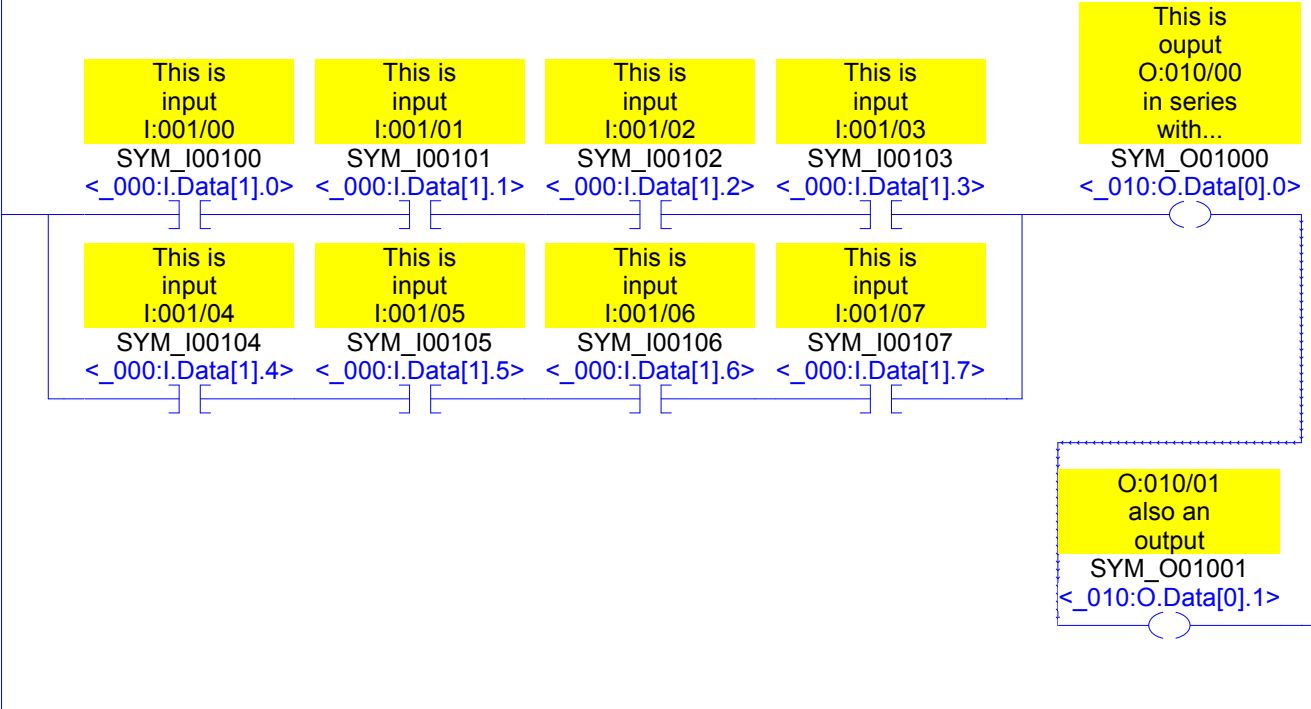


This is a rung comment. This section of rungs show how BITS are converted.

This program is totally useless...
 as far as controlling any process. Now on the other hand if you would like to see how different instructions are converted read on.

0

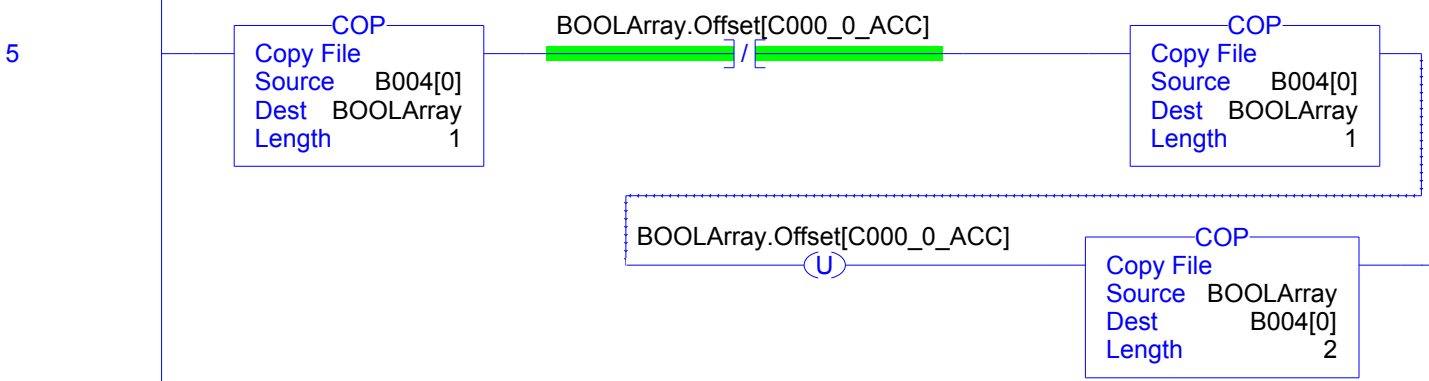
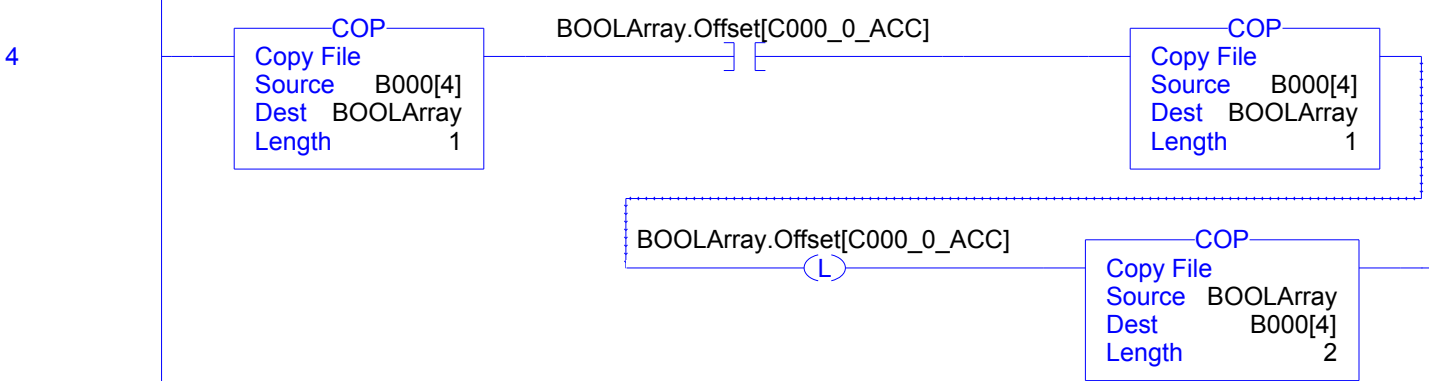
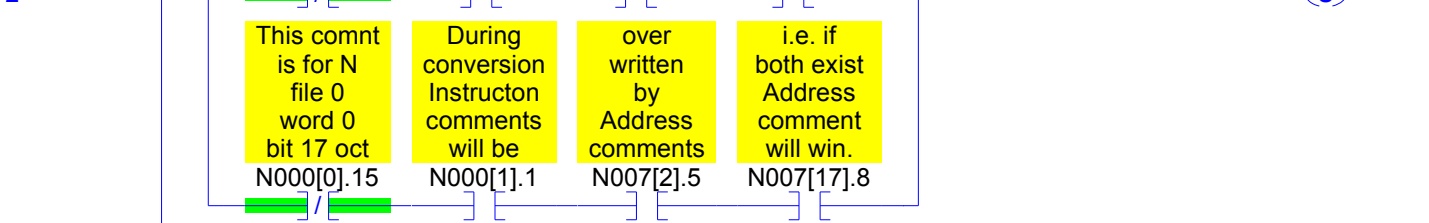
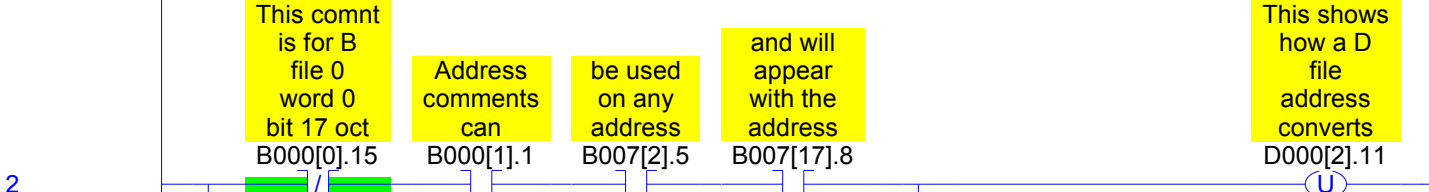


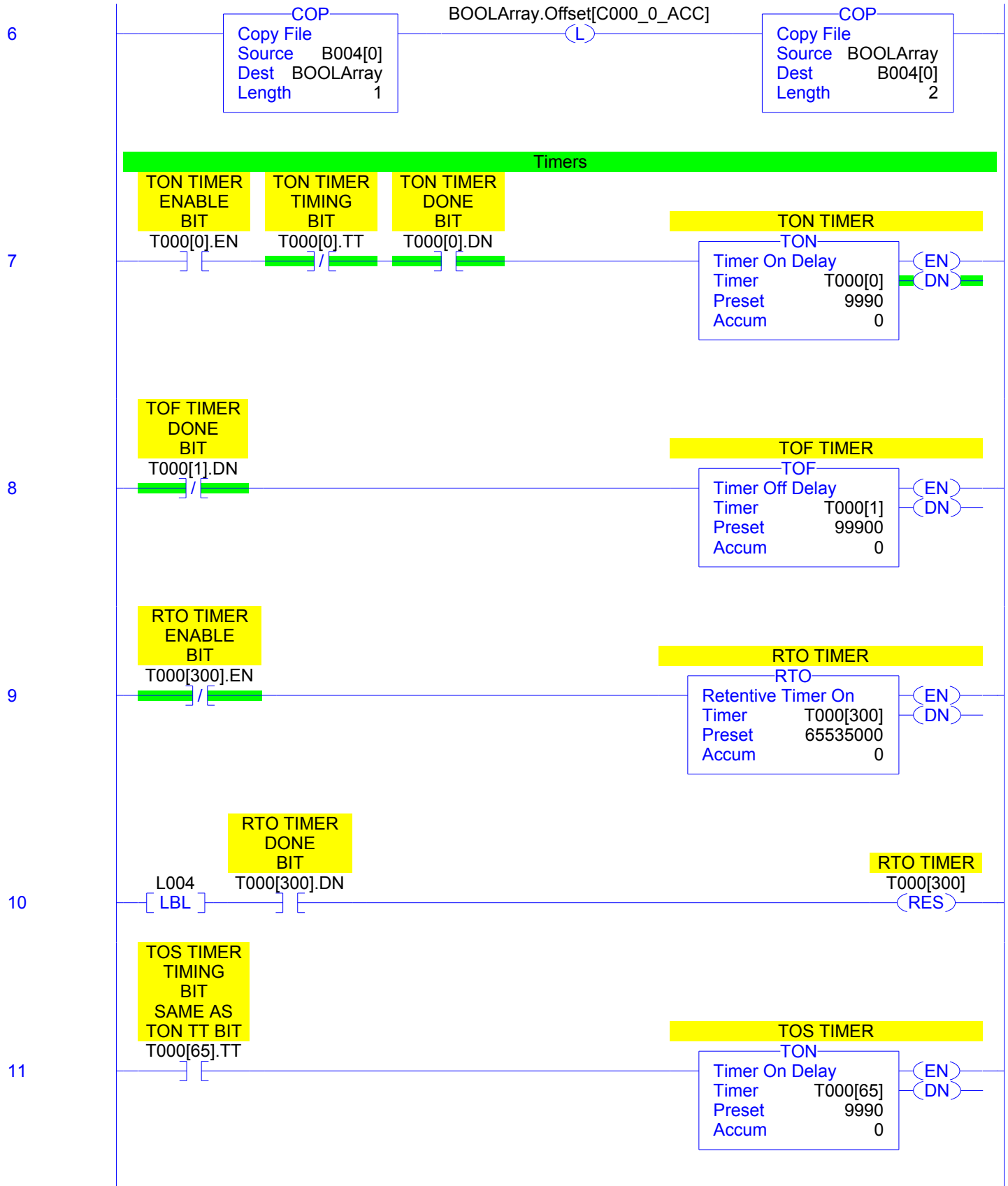
This program was converted using the DHRIO option. Bits in the rack range will be remapped to I/O. Bits outside of this range will point to the INT array (I000 or O000).

1

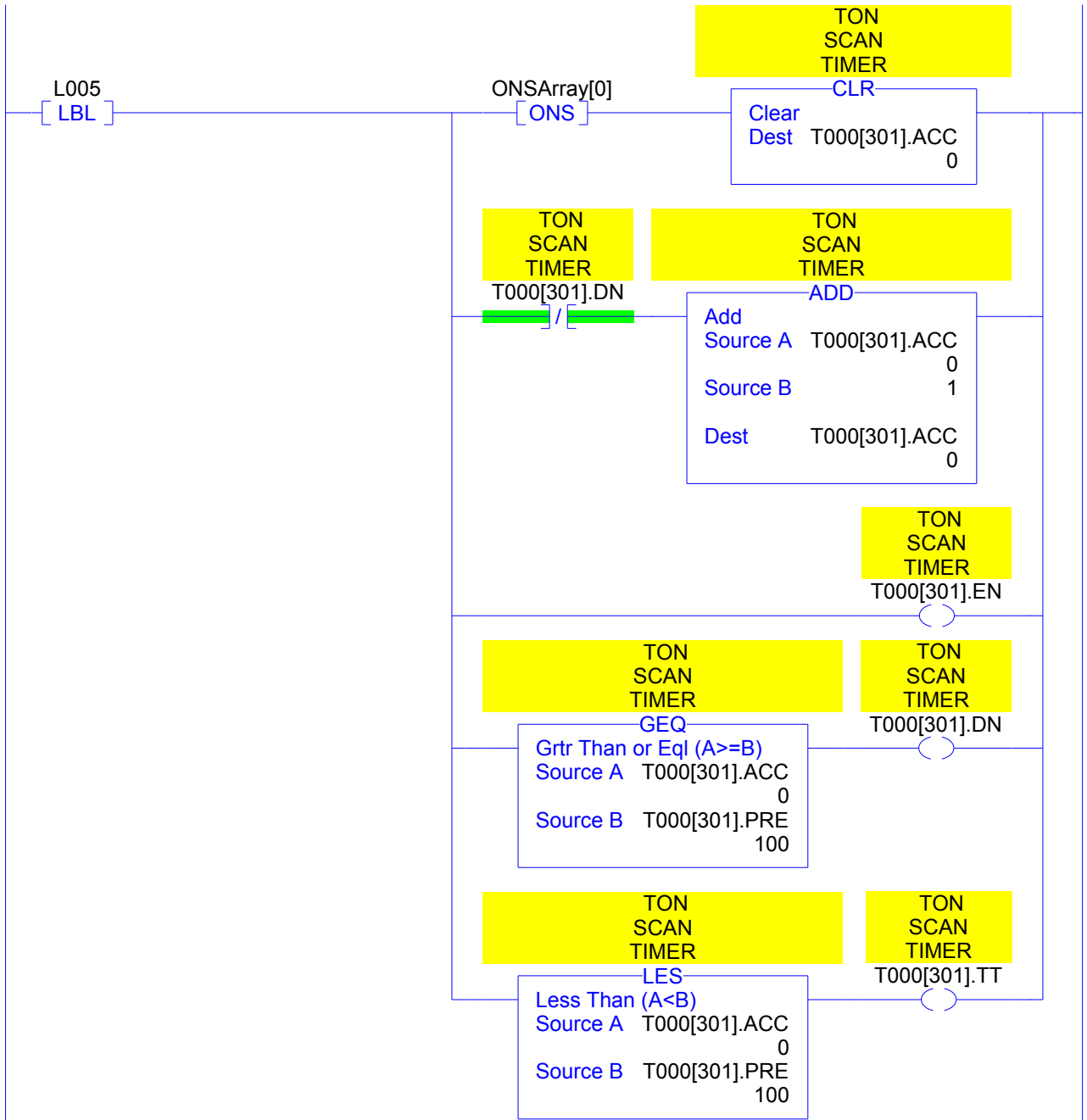


I/O bit addresses use the format: **_RRS:I.Data[M].B**
 where: RR = rack in octal
 S = Starting module group
 I = Input (or O for output)
 M = Module group offset
 B = Terminal number in decimal

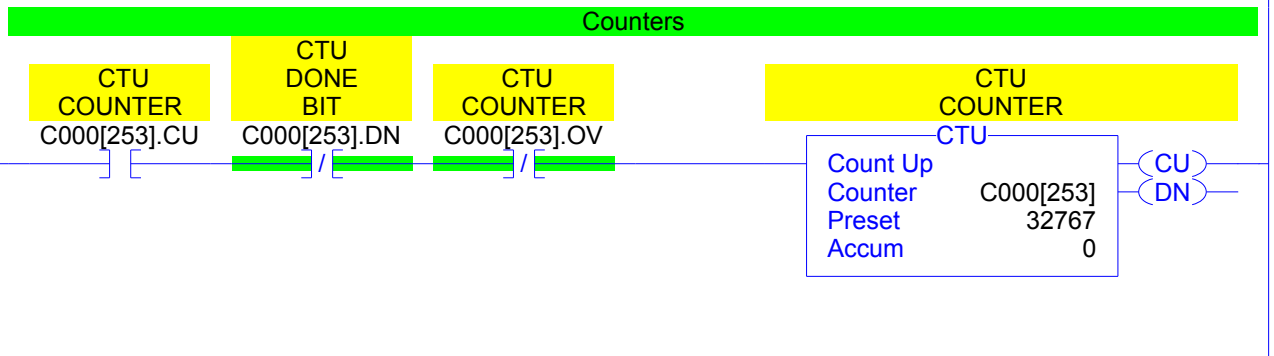


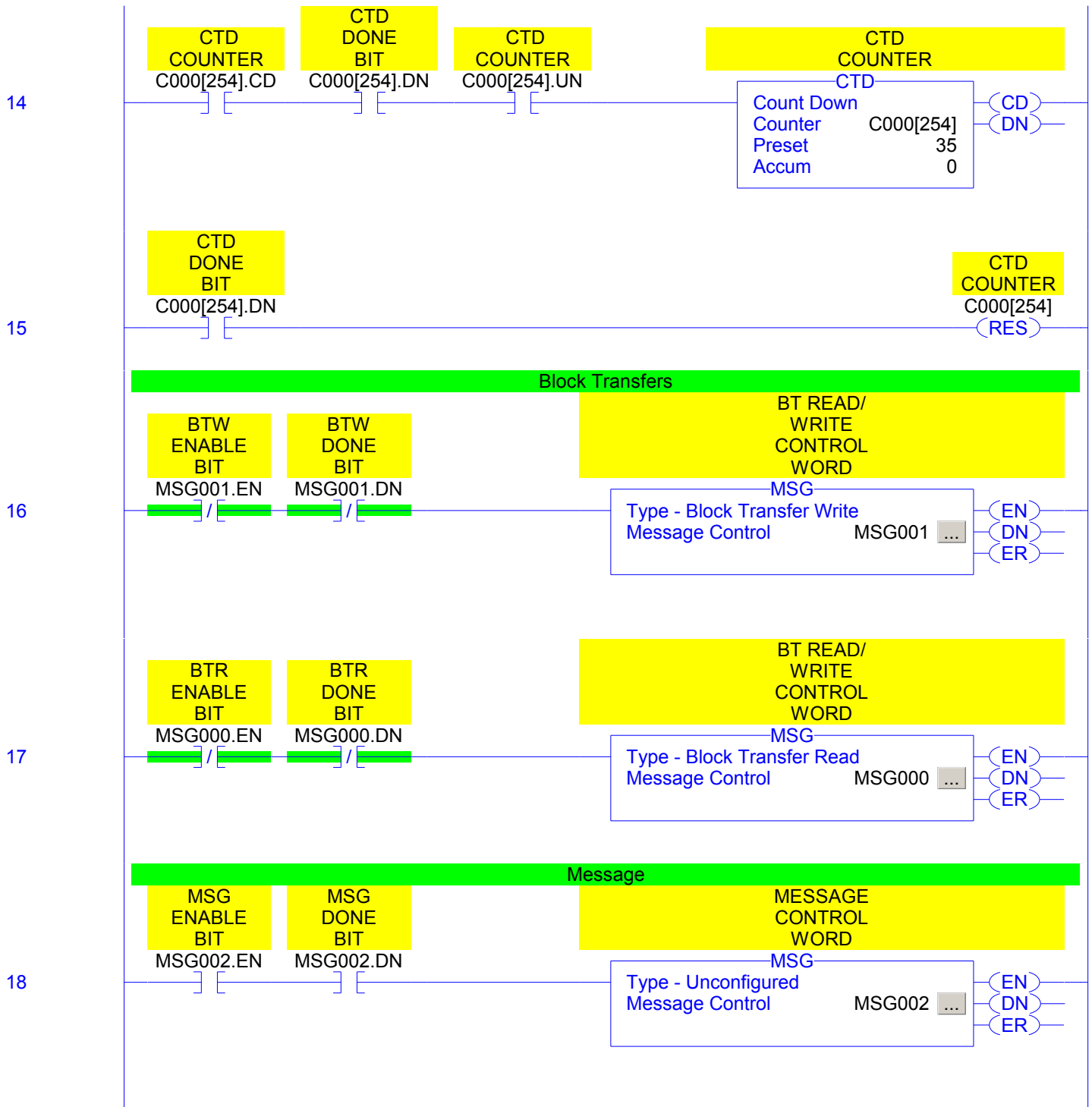


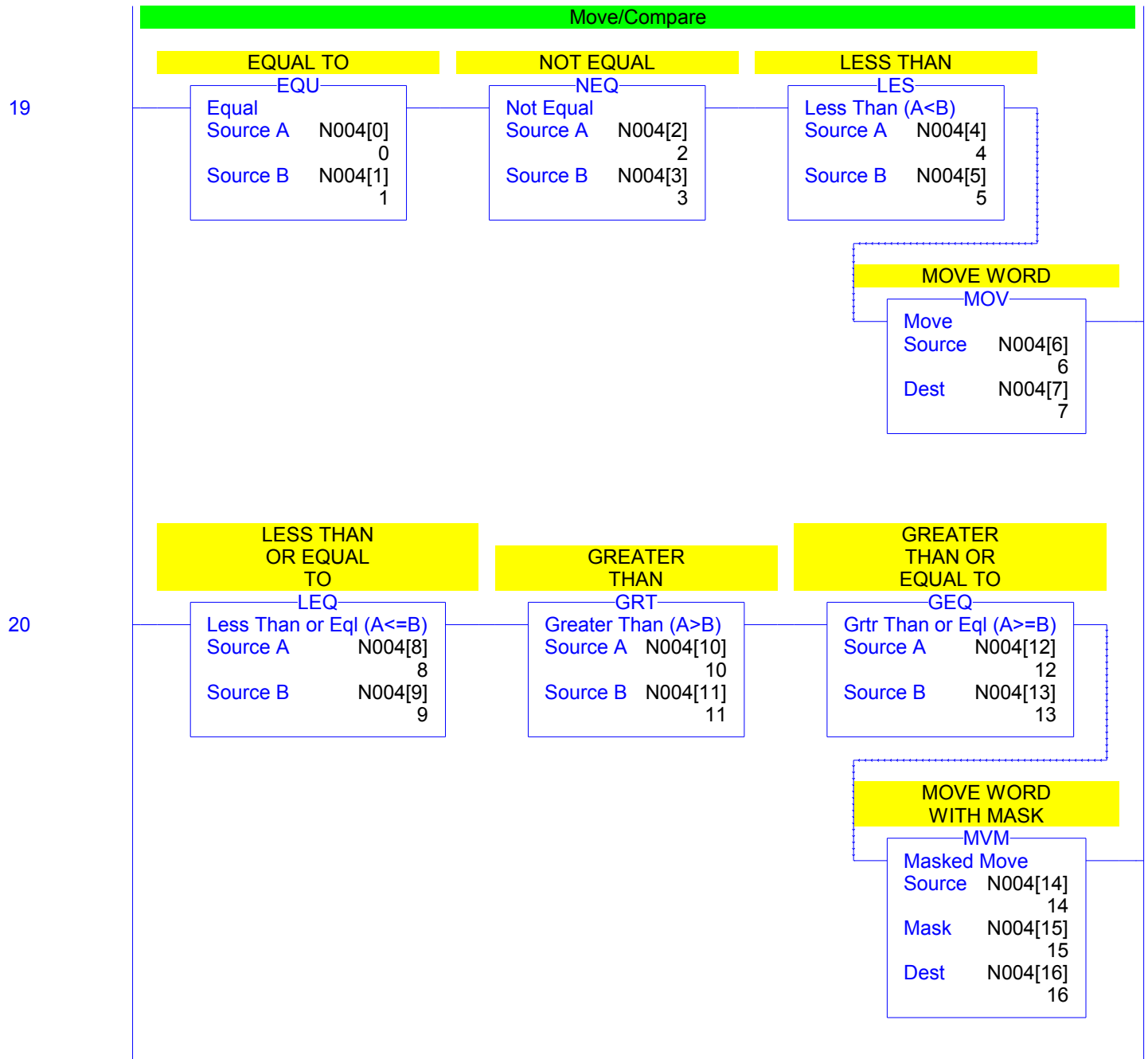
12

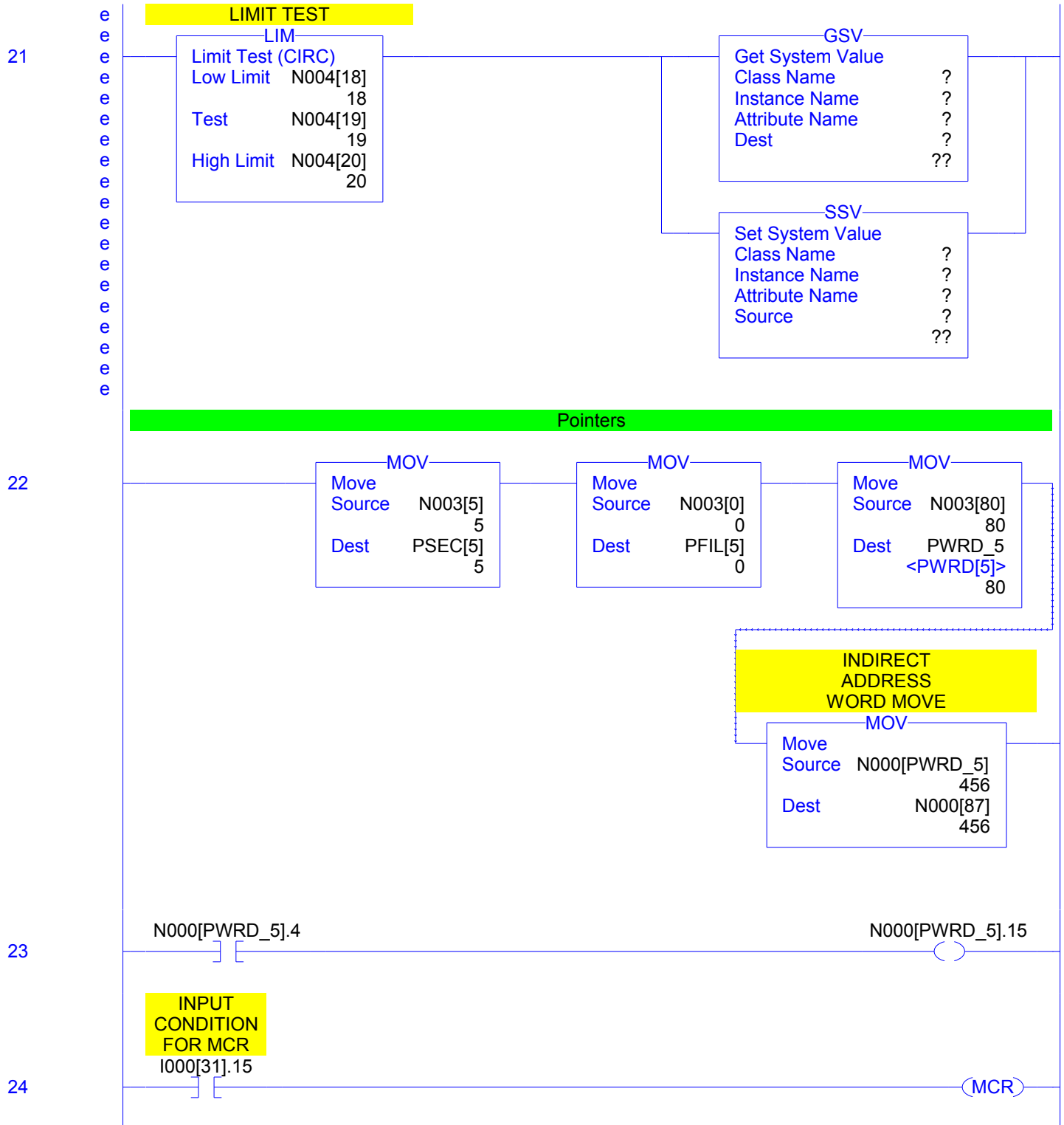


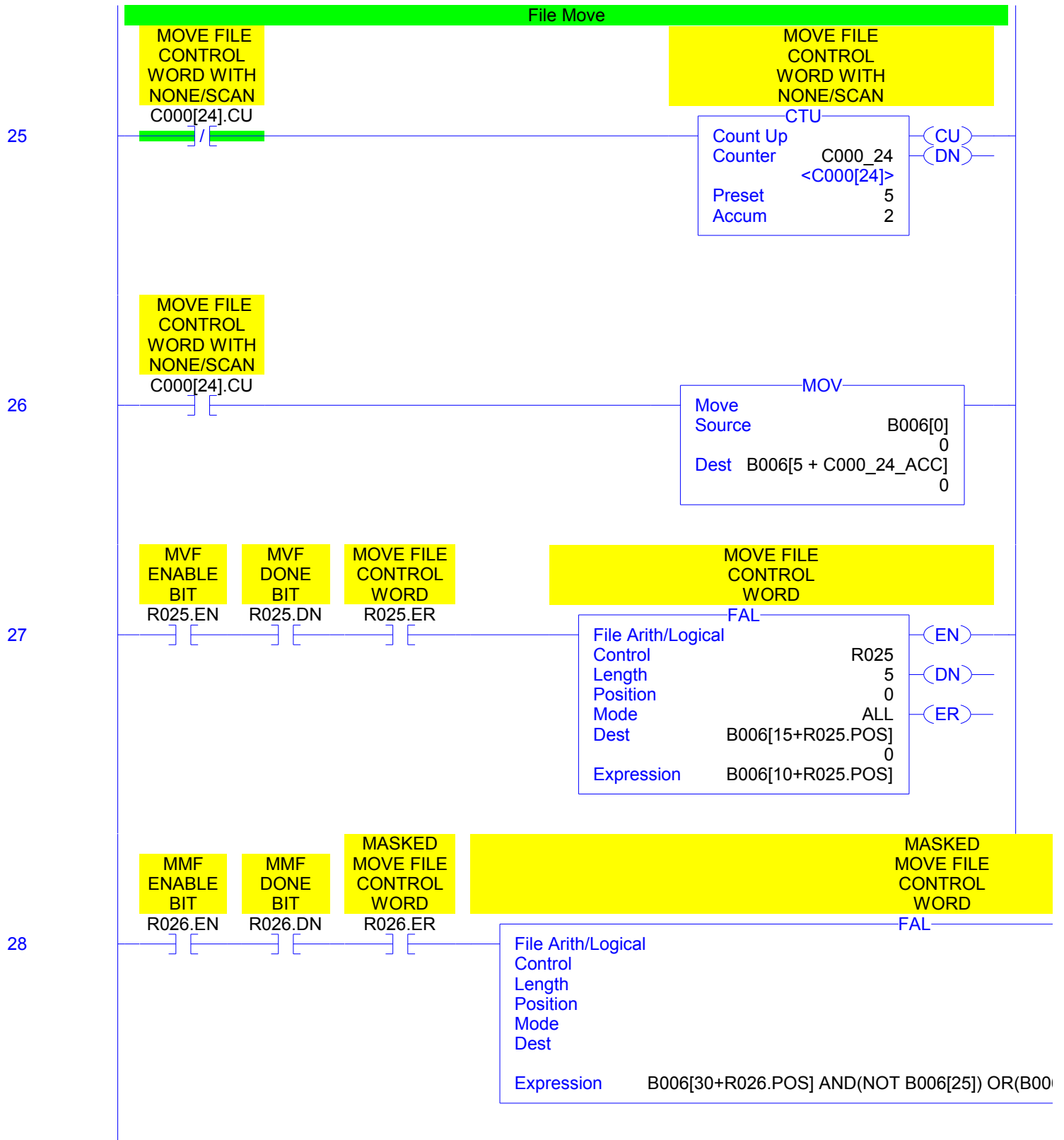
13

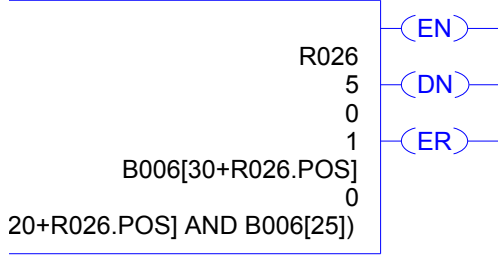










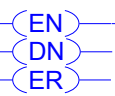
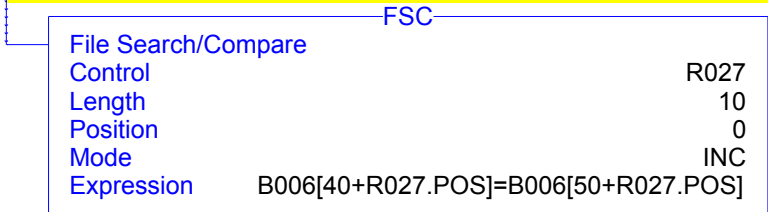


29

(MCR)



30



31

SNE
ENABLE
BIT
R028.EN

SNE
DONE
BIT
R028.DN

SNE
FOUND
BIT
R028.FD

FILE
SEARCH NOT
EQUAL
CONTROL
WORD

FSC

File Search/Compare	
Control	R028
Length	10
Position	0
Mode	ALL
Expression	B006[60+R028.POS]<->B006[70+R028.POS]

(EN)
(DN)
(ER)

32

SLS
ENABLE
BIT
R029.EN

SLS
DONE
BIT
R029.DN

SLS
FOUND
BIT
R029.FD

FILE
SEARCH
LESS THAN
CONTROL
WORD

FSC

File Search/Compare	
Control	R029
Length	10
Position	0
Mode	1
Expression	B006[80+R029.POS]<B006[90+R029.POS]

(EN)
(DN)
(ER)

33

SLE
ENABLE
BIT
R030.EN

SLE
DONE
BIT
R030.DN

SLE
FOUND
BIT
R030.FD

FILE
SEARCH
LESS/EQUAL
CONTROL
WORD

FSC

File Search/Compare	
Control	R030
Length	10
Position	0
Mode	ALL
Expression	B006[100+R030.POS]<=B006[110+R030.POS]

(EN)
(DN)
(ER)

34

SGR
ENABLE
BIT
R031.EN

SGR
DONE
BIT
R031.DN

SGR
FOUND
BIT
R031.FD

FILE
SEARCH
GREATER
CONTROL
WORD

FSC

File Search/Compare	
Control	R031
Length	10
Position	0
Mode	ALL
Expression	B006[120+R031.POS]>B006[130+R031.POS]

(EN)
(DN)
(ER)

35

SGE ENABLE BIT R032.EN SGE DONE BIT R032.DN SGE FOUND BIT R032.FD

FILE SEARCH GRTR/EQUAL CONTROL WORD

FSC

File Search/Compare
 Control R032
 Length 10
 Position 0
 Mode ALL
 Expression B006[140+R032.POS]>=B006[150+R032.POS]

EN
 DN
 ER

Math

36

ADDITION

ADD

Add
 Source A N004[30] 1
 Source B N004[31] 1
 Dest N004[32] 2

37

SUBTRACTON

SUB

Subtract
 Source A N004[33] 7
 Source B N004[34] 4
 Dest N004[35] 3

38

MULTIPLCTN

MUL

Multiply
 Source A H006[1] 999
 Source B H006[2] 1000
 Dest H006[9] 999000

