

Long Comment for NETWORK 0001 Segment: 01 Attached To: Network

This program is totally useless...

as far as controlling any process. Now on the other hand if you would like to see how different instructions are converted read on.

This program was converted using the ANX option. The registers convert to alias' that point to the AN-X module. The AN-X CSV file has been imported into the ControlLogix program.

This section of rungs show how bits are converted.

NETWORK 0001 Segment: 01 (LONG COMMENT ON PREVIOUS PAGE)

I This is t descripti for N0001

This is the short comment for N0001

```

I I
I I
I I
I I
I This is t
I descripti
I for 10001
I DIR1S03
1+--] [-----]
I SYM_10001
I 10001
I I
I I
I This is t
I descripti
I for 10002
I DIR1S03
2+--] [-----]
I SYM_10002
I 10002
I I
I I
I This is t
I descripti
I for 10003
I DIR1S03
3+--] [-----]
I SYM_10003
I 10003
I I
I I
I This is t
I descripti
I for 10004
I DIR1S03
4+--] [-----]
I SYM_10004
I 10004
I I
I I
I This is t
I descripti
I for 10005
I DIR1S03
5+--] [-----]
I SYM_10005
I 10005
I I
I I
I This is t
I descripti
I for 10006
I DIR1S03
6+--] [-----]
I SYM_10006
I 10006
I I
I I
I This is t
I descripti
I for 10007
I DIR1S03
7+--] [-----]
I SYM_10007
I 10007
I I
I I
I This is t
I descripti
I for 10008
I DIR1S03
8+--] [-----]
I SYM_10008
I 10008
I I
I I
I This is t
I descripti
I for 10009
I DIR1S03
9+--] [-----]
I SYM_10009
I 10009
I I
I I
I This is t
I descripti
I for 10010
I DIR1S03
10+--] [-----]
I SYM_10010
I 10010
I I
I I
I This is t
I descripti
I for 10011
I DIR1S03
11+--] [-----]
I SYM_10011
I 10011
I I
I I
I This is t
I descripti
I for 10012
I DIR1S03
12+--] [-----]
I SYM_10012
I 10012
I I
I I
I This is t
I descripti
I for 10013
I DIR1S03
13+--] [-----]
I SYM_10013
I 10013
I I
I I
I This is t
I descripti
I for 10014
I DIR1S03
14+--] [-----]
I SYM_10014
I 10014
I I
I I
I This is t
I descripti
I for 10015
I DIR1S03
15+--] [-----]
I SYM_10015
I 10015
I I
I I
I This is t
I descripti
I for 10016
I DIR1S03
16+--] [-----]
I SYM_10016
I 10016
I I
I I
I This is t
I descripti
I for 10001
I DIR1S04
17+--] [-----]
I SYM_10001
I 10001
I I
I I
I This is t
I descripti
I for 00002
I DIR1S04
18+--] [-----]
I SYM_00002
I 00002
I I
I I
I This is t
I descripti
I for 00003
I DIR1S04
19+--] [-----]
I SYM_00003
I 00003
I I
I I
I This is t
I descripti
I for 00004
I DIR1S04
20+--] [-----]
I SYM_00004
I 00004
I I
I I
I This is t
I descripti
I for 00005
I DIR1S04
21+--] [-----]
I SYM_00005
I 00005

```

NETWORK 0002 Segment: 01

```

I I This is a This is a This is a This is a
I positive positive positive positive
I transitio transitio transitio transitio
I for 10017 not as th not as th not as th not as th
1+--JP[-----]P[-----]P[-----]P[-----]
I 10017 10018 10019 10020
I I I I
I I This is a This is a This is a This is a
I This is a This is a This is a This is a
I negative negative negative negative
I transitio transitio transitio transitio
I for 10021 not as th not as th not as th not as th
2+--JN[-----]N[-----]N[-----]N[-----]*
I 10021 10022 00006 00007

```

```

This is t
descripti
for 00030
( )-----
00030

```

3+

```

I I This is t This is t This is t This is t
I descripti descripti descripti descripti
I for 10023 for 10024 for 10025 for 10032
4+--] [-----] [-----] [-----] [-----]
I 10023 10024 10025 10032
I I

```

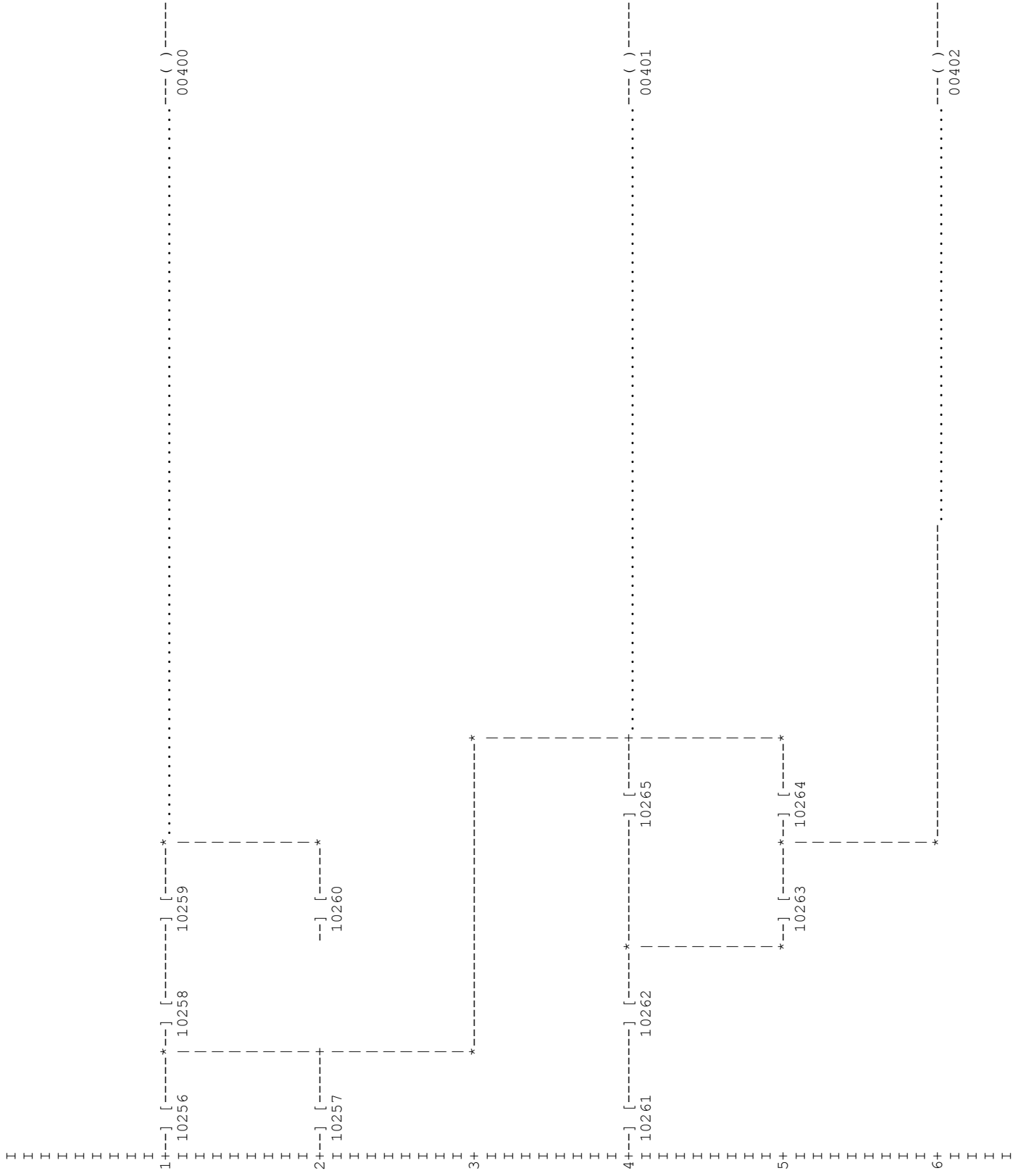
```

This is a
retentive
coil
-----] [-----] [-----] [-----]
00024

```

Long Comment for NETWORK 0004 Segment: 01 Attached To: Network
 Networks like this, although valid in the Modicon, do not convert well
 into ControlLogix. Manual clean up will be needed with this network.

NETWORK 0004 Segment: 01 (LONG COMMENT ON PREVIOUS PAGE)



.....()-----
00403

<<PromORX>> PWP Network 0004 Page 00004

NETWORK 0005 Segment: 02

```
I
I
I
I
I
I
I
I
I
I UC'tR
I Control
1+-] [-----*-----*
I 10033 |-----*-----*-----*-----*
I |#1000 |
I |-----*-----*-----*-----*
I |-----*-----*-----*-----*
I |-----*-----*-----*-----*
I |-----*-----*-----*-----*
I |-----*-----*-----*-----*
I |Up Counte | UC'tR +-----*-----*
2+-] [-----*-----*
I 40001 |
I
I
I
I
I
I
I
I
I DC'tR
I Control
3+-] [-----*-----*-----*-----*
I 10034 |40002 |
I |-----*-----*-----*-----*
I |-----*-----*-----*-----*
I |-----*-----*-----*-----*
I |-----*-----*-----*-----*
I DC'tR
I Enable/Re |Down Coun |
4+-] [-----*-----*-----*-----*
I 10035 | DCTR +-----*-----*
I 40003 |
I
I
```

UC'tR
Count=Pre
DIRIS04
---- () -----
00008

UC'tR
Count<Pre
DIRIS04
---- () -----
00009

DC'tR
Count=0
DIRIS04
---- () -----
00010

DC'tR
Count>0 a
Count<Pre
DIRIS04
---- () -----
00011


```
I
I
I T.01
I Control ||Timer 0.0
7+--] [-----*+ T.01 +-----
I 10039 40007
I
I
```

```
T.01
Timer<Pre
DIRIS04
--( )-----
00015
```


I |
I |
I |Addition|
I |instructi
7++ ADD +-
I 40008
I
I

NETWORK 0008 Segment: 02

When the SUB result contains the optional "Trash Register" the SUB instruction is removed and

Address	Instruction	Disassembly
10040	I Control	
10041	I SUB	SUB Top>Middl 00041
20100	I SUB	SUB Top=Middl 00042
40999	I SUB	SUB Top<Middl 00043
40041	I SUB	SUB Top>=Midd 00044

NETWORK 0009 Segment: 02

This will show what will happen to SUB instructions that do not contain the "Trash

Line	Instruction	Address	Label
1	I SUB	40018	
1	I Control		
1	I 10042		
2	I +		
2	I #0100		
3	I Trash		
3	I Register		
3	I + SUB		
3	I 40999		
4	I SUB	40019	
4	I Control		
4	I 10043		
5	I +		
5	I 40020		
6	I SUB		
6	I Instructi		
6	I + SUB		
6	I 40021		

SUB
Top<=Midd
()-----
00045

SUB
Top>Middl
()-----
00046

SUB
Top=Middl
()-----
00047

SUB
Top<Middl
()-----
00048

I
I
I
I
I
7+
I
I
I

+
OR
#0001

+
OR
#0020

I
7+
I
I
I
I

I
+
I
+
XOR
#0020
+-

I
+
I
+
XOR
#0001
+-

NETWORK 0012 Segment: 03

I					
I					
I					
I					
I					
I					
I					
I	SKP				
I	Control				
1+-] \ [-	SKP	+-			
I	10026	#0002			
I					
I					
I					
I					
I					
I	R->T				
I	Control				
2+-] \ [-	*	10081			Passes Power () 00034
I	10027				
I					
I					
I					
I	R->T				
I	Prevent				
I	Pointer	R->T			
I	Increment	on one re			
3+-] \ [-	*	40188			R->T Pointer a top of ta () 00035
I	10028				
I					
I					
I					
I					
I					
I	R->T				
I	Reset				
4+-] \ [-	*	R->T	+-		
I	10029	#0001			
I					
I					
I					
I					
I	R->T				
I	Control				
5+-] \ [-	*	40190			Passes Power () 00036
I	10030				
I					
I					
I	R->T				
I	Prevent	R->T			
I	Pointer	on 20			
I	Increase	registers			
6+-] \ [-	*	40191			R->T Pointer a top of ta () 00037
I	10031				
I					
I					

```
I | | | |
I | | | |
I R->T | | | |
I Reset | | | |
7+--+ ] [-----+ R->T +-
I 10044 #0020
I
I
```

NETWORK 0013 Segment: 03

```

I
I
I
I
I T->R
I Control
1+--] [-----*-----]-----*-----
I 10045 |40212 |
I |
I |
I |
I T->R
I Prevent
I Pointer |T->R
I Increase |on one re
2+--] [-----+-----]-----*-----
I 10046 |40213 |
I |
I |
I |
I |
I |
I T->R
I Reset
I 10047 |-----+-----]-----*-----
3+--] [-----+-----]-----*-----
I 10047 |-----+-----]-----*-----
I |
I |
I T->R
I Control
4+--] [-----*-----]-----*-----
I 10048 |40215 |
I |
I |
I |
I T->R
I Prevent |T->R
I Pointer |on 20
I Increase |registers
5+--] [-----+-----]-----*-----
I 10113 |40216 |
I |
I |
I |
I |
I |
I T->R
I Reset
I 10114 |-----+-----]-----*-----
6+--] [-----+-----]-----*-----
I 10114 |-----+-----]-----*-----
I |
I |

```

Passes
Power

()-----
00038

T->R
Pointer a
top of ta

()-----
00039

Passes
Power

()-----
00040

T->R
Pointer a
top of ta

()-----
00065

NETWORK 0014 Segment: 03

```

I
I
I
I
I T->T
I Control
1+--] [-----*-----+-----+-----+-----+-----+
I 10115 |40235 |
I |
I |
I |
I T->T
I Prevents |
I Pointer |T->T |
I Increase |on one re |
2+--] [-----+-----+-----+-----+-----+
I 10116 |40236 |
I |
I |
I |
I |
I |
I T->T
I Reset |
3+--] [-----+-----+-----+-----+-----+
I 10117 |         |T->T |
I |         |#0001 |
I |
I |
I |
I |
I T->T
I Control
4+--] [-----*-----+-----+-----+-----+-----+
I 10118 |40238 |
I |
I |
I |
I T->T
I Prevents |T->T |
I Pointer |on 20 |
I Increase |registers |
5+--] [-----+-----+-----+-----+-----+
I 10119 |40258 |
I |
I |
I |
I |
I |
I T->T
I Reset |
6+--] [-----+-----+-----+-----+-----+
I 10120 |         |T->T |
I |         |#0020 |
I |
I |

```

```

Passes
Power
-----+-----+-----+-----+-----+
( )
00066

T->T
Pointer a
top of ta
-----+-----+-----+-----+-----+
( )
00067

Passes
Power
-----+-----+-----+-----+-----+
( )
00068

T->T
Pointer a
top of ta
-----+-----+-----+-----+-----+
( )
00069

```


NETWORK 0018 Segment: 04

I				
I				
I				
I	I	CMPR		Passes
I	I	Control		Power
1+--]	[-----*	40337		-----
I	I	I		()-----
I	I	I		00078
I	I	I		
I	I	CMPR		
I	I	Reset		CMPR
I	I	Pointer		miscompar
2+--]	[-----*	40339		-----
I	I	I		()-----
I	I	I		00032
I	I	I		
I	I	I		
I	I	I		
I	I	I		
I	I	I		
I	I	CMPR		CMPR
3+	+-----*	CMPR		state of
I	I	#0001		miscompar
I	I	I		-----
I	I	I		()-----
I	I	I		00033

NETWORK 0019 Segment: 04

```

I
I
I
I
I SENS          SENS          Passes
I Control      instructio Power
1+--] [-----*-----]-----
I 10125 |40338
I
I
I
I
I SENS          SENS          SENS
I Increase    bit result bit result
I Pointer     ( ) ( ) ( )
2+--] [-----+-----]-----
I 10126 |30002
I
I
I
I
I SENS          SENS          SENS
I Reset       error error
I Pointer     ( ) ( ) ( )
3+--] [-----+-----]-----
I 10127 |SENS #0016
I
I
I
I
I MBIT         MBIT         Passes
I Control      instructi Power
4+--] [-----*-----]-----
I 10128 |40341
I
I
I
I
I MBIT         MBIT         MBIT
I Set/Clear   sense bit sense bit
I Control     ( ) ( ) ( )
5+--] [-----+-----]-----
I 10129 |40357
I
I
I
I
I MBIT         MBIT         MBIT
I Increase    error error
I Pointer     ( ) ( ) ( )
6+--] [-----+-----]-----
I 10130 |MBIT #0032
I
I

```


NETWORK 0022 Segment: 04

I PID			
I Auto/Manu Source		PID2 Loop Inva	
1+--] [-----*	-----*	-----*	-----*
I 10140	40546	00321
I		
I		
I		
I		
I		
I		
I		
I PID2	PID2	
I Track Mod	Destinati	
2+--] [-----* <th>-----*</th> <th>-----*</th> <th>-----*</th>	-----*	-----*	-----*
I 10141	40567	00322
I		
I		
I		
I		
I		
I		
I		
I PID2		
I Action		
3+--] [-----* <th>-----*</th> <th>-----*</th> <th>-----*</th>	-----*	-----*	-----*
I 10142	PID2	00323
I	#0001	
I		
I		

NETWORK 0023 Segment: 04

```
I  
I  
I  
I  
I  
I  
I  
1+-----*-----*--  
I |L001 |  
I | | |  
I | | | |  
I | | | | |  
I | | | | | |  
I | | | | | | |  
I | | | | | | | |  
2+ | | | | | | | | |  
I | | | | | | | | | JSR +-  
I | | | | | | | | | #0001  
I | | | | | | | | | |
```

NETWORK 0024 Segment: 05

Passes Power	BLKT Move Complete	BLKT Error	TBLK Move Complete	TBLK Error
-----	-----	-----	-----	-----
1++ LAB				
I L001				
I				
I				
I				
I				
I				
I				
I BLKT	BLKT			
I Control	Source Bl			
2+--] [-----*	40576			
I 10143				
I				
I				
I				
I BLKT	BLKT			
I Hold Poin	Destinati			
I Pointer				
3+--] [-----*	40586			
I 10144				
I				
I				
I BLKT	BLKT			
I Reset Poi				
4+--] [-----*	BLKT +--			
I 10145	#0010			
I				
I				
I				
I TBLK	TBLK			
I Source	Table			
5+--] [-----*	40597			
I 10146				
I				
I				
I				
I TBLK	TBLK			
I Hold Poin	Destinati			
I Pointer				
6+--] [-----*	40610			
I 10147				
I				
I				
I				
I TBLK	TBLK			
I Reset				

```
I Pointer | |
7+--} [------+ TBLK +-
I 10148 #0010
I
I
```

=====
NETWORK 0025 Segment: 05

I
I
I
I
I
I
I
1++ RET +-
I #0001
I
I

