

TITLE

This program is totally useless...

as far as controlling any process. Now on the other hand if you would like to see how different instructions are converted read on.

This program was converted using the ANX option. Bits in the I/O Configuration will be remapped to AN-X-TIMAS I/O. Bits outside of this range will point to the BOOL array (IO) or the INT array (WIO).

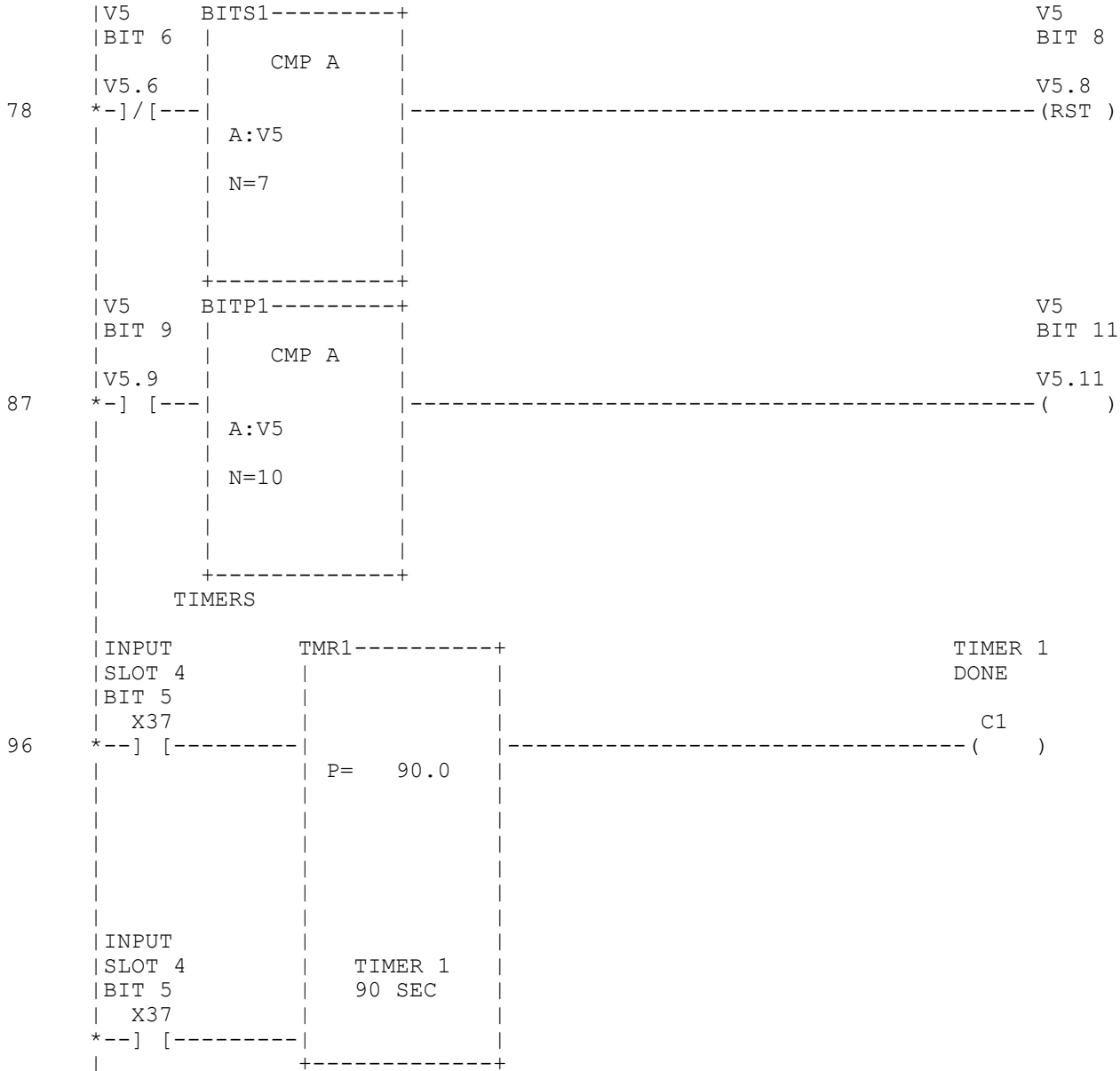
This is a raw conversion except that the ANX import file has been imported.

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DATE: 04-17-09
VERSION: 1.0


```
| INPUT  INPUT                                OUTPUT  
| SLOT 1  SLOT 1                                SLOT 6  
| BIT 9   BIT 10                               BIT 8  
|  X25    X26      1                            Y96  
25 *--] [----]/[-*]:O:----- (RST )  
|  
| INPUT  INPUT |  
| SLOT 1  SLOT 1 |  
| BIT 11  BIT 12 |  
|  X27    X28 |  
*--] [----]/[-+  
|  
| INPUT  INPUT  INPUT  INPUT                                OUTPUT  
| SLOT 1  SLOT 1  SLOT 1  SLOT 1                                SLOT 6  
| BIT 13  BIT 14  BIT 15  BIT 16                               BIT 9  
|  X29    X30    X31    X32                                    Y97  
34 *--] [----] [----] [----] [---:NOT:----- (RST )  
|  
| OUTPUT                                OUTPUT  
| SLOT 6                                SLOT 6  
| BIT 1                                  BIT 10  
|  Y89                                    Y98  
43 *--] [-*----- ( / )  
|  
| OUTPUT |  
| SLOT 6 |  
| BIT 2  |  
|  Y90  |  
*--] [-*  
|  
| OUTPUT |  
| SLOT 6 |  
| BIT 3  |  
|  Y91  |  
*--] [-*  
|  
| OUTPUT |  
| SLOT 6 |  
| BIT 4  |  
|  Y92  |  
*--] [-+  
|
```

	INPUT	INPUT	INPUT	INPUT	OUTPUT	
	SLOT 4	SLOT 4	SLOT 4	SLOT 4	SLOT 6	
	BIT 1	BIT 2	BIT 3	BIT 4	BIT 11	
	X33	X34	X35	X36	Y99	
48	*--]	[-----]	/[-----]	[-----]	/[-----]	*-- ()
					OUTPUT	
					SLOT 6	
					BIT 12	
					Y100	
					*-- ()	
					OUTPUT	
					SLOT 6	
					BIT 13	
					Y101	
					*-- ()	
					OUTPUT	
					SLOT 6	
					BIT 14	
					Y102	
					*-- ()	
					OUTPUT	
					SLOT 6	
					BIT 15	
					Y103	
					+-- ()	
	V5				V5 SECOND	
	LEAST SIG				LEAST SIG	
	BIT				BIT	
	V5.16				V5.15	
57	*--]	[-----]	/[-----]	[-----]	/[-----]	*-- ()
	V5				V5 SECOND	
	MOST SIG				MOST SIG	
	BIT				BIT	
	V5.1				V5.2	
63	*--]	[-----]	/[-----]	[-----]	/[-----]	*-- ()
	V5	BITC1-----+			V5	
	BIT 3				BIT 5	
		CMP A				
	V5.3				V5.5	
69	*--]	[-----]	/[-----]	[-----]	/[-----]	(SET)
		A:V5				
		N=4				

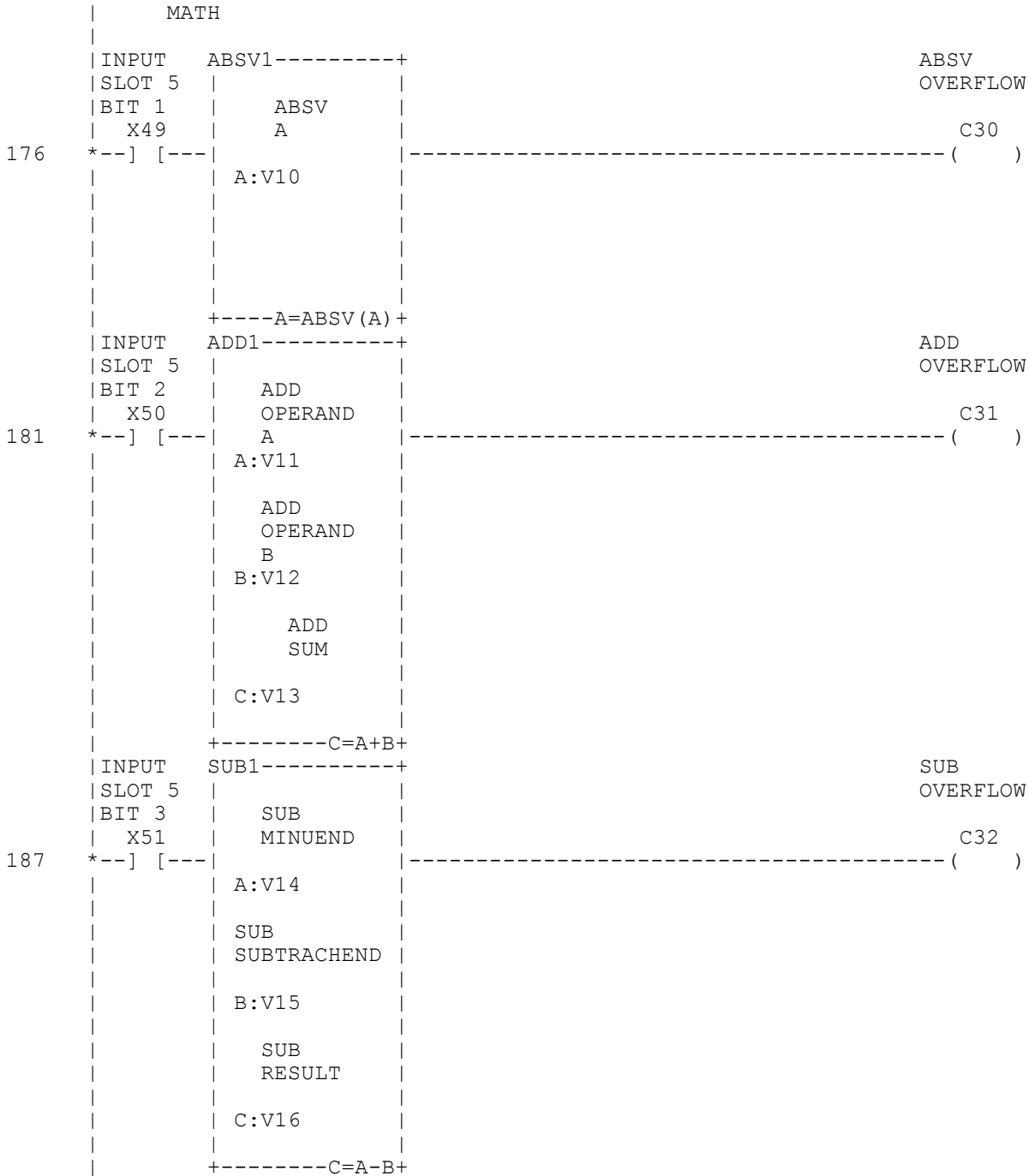


	INPUT	TMRF2-----+		TIMER 2
	SLOT 4			DONE
	BIT 6			
	X38			C2
101	*--] [-----		P= 12.345	()
	INPUT		TIMER 2	
	SLOT 4		12.345	
	BIT 7		SECONDS	
	X39			
	*--] / [-----			
		+		
	COUNTERS			
	INPUT	CTR3-----+		COUNTER 3
	SLOT 4			DONE
	BIT 8			
	X40			C3
106	*--] [-----		P= 4000	()
	INPUT		COUNTER 3	
	SLOT 4			
	BIT 9			
	X41			
	*--] / [-----			
		+		
	INPUT	UDC4-----+		COUNTER 4
	SLOT 4			DONE
	BIT 10			
	X42			C5
111	*--] [-----		P= 250	()
	INPUT		COUNTER 4	
	SLOT 4		EQUALS	
	BIT 11		ZERO	
	X43		Z:C4	
	*--] [-----			
	INPUT		COUNTER 4	
	SLOT 4		UP/DOWN	
	BIT 12			
	X44			
	*--] / [-----			
		+		

```

|             COMPARE
|
|ANALOG IN ANALOG OUT                      EQUAL
|SLOT 11   SLOT 13
|WORD 1    WORD 1
|   WX105  WY1                               C10
118 *-----] = INT[-----] (      )
|
|ANALOG IN ANALOG OUT                      NOT EQUAL
|SLOT 11   SLOT 13
|WORD 2    WORD 2
|   WX106  WY2                               C11
123 *-----]<> INT[-----] (      )
|
|ANALOG IN ANALOG OUT                      LESS THAN
|SLOT 11   SLOT 13
|WORD 3    WORD 3
|   WX107  WY3                               C12
128 *-----] < INT[-----] (      )
|
|ANALOG IN ANALOG OUT                      GREATER
|SLOT 11   SLOT 13                      THAN OR
|WORD 4    WORD 4                      EQUAL
|   WX108  WY4                               C13
133 *-----]>= INT[-----] (      )
|
|ANALOG IN ANALOG OUT                      GREATER
|SLOT 11   SLOT 13                      THAN
|WORD 5    WORD 5
|   WX109  WY5                               C14
138 *-----] > INT[-----] (      )
|
|ANALOG IN ANALOG OUT                      LESS THAN
|SLOT 11   SLOT 13                      OR EQUAL
|WORD 6    WORD 6
|   WX110  WY6                               C15
143 *-----]<= INT[-----] (      )
|
|INPUT   CMP1-----+                     CMP
|SLOT 4  |           |                     A=B
|BIT 13  |           |
|   X45  |   CMP A   |
|         |           |                     C16
148 *--] [---|-----] (      )
|         |           |
|         |   A:V1   |
|         |           |
|         |   CMP B  |
|         |           |
|         |   B:V2   |
|         |           |
|         |           |
|         |           |
|         |   LT:    |
|         |   GT:    |
|         |   +LT=A<B;GT=A>B+
  
```

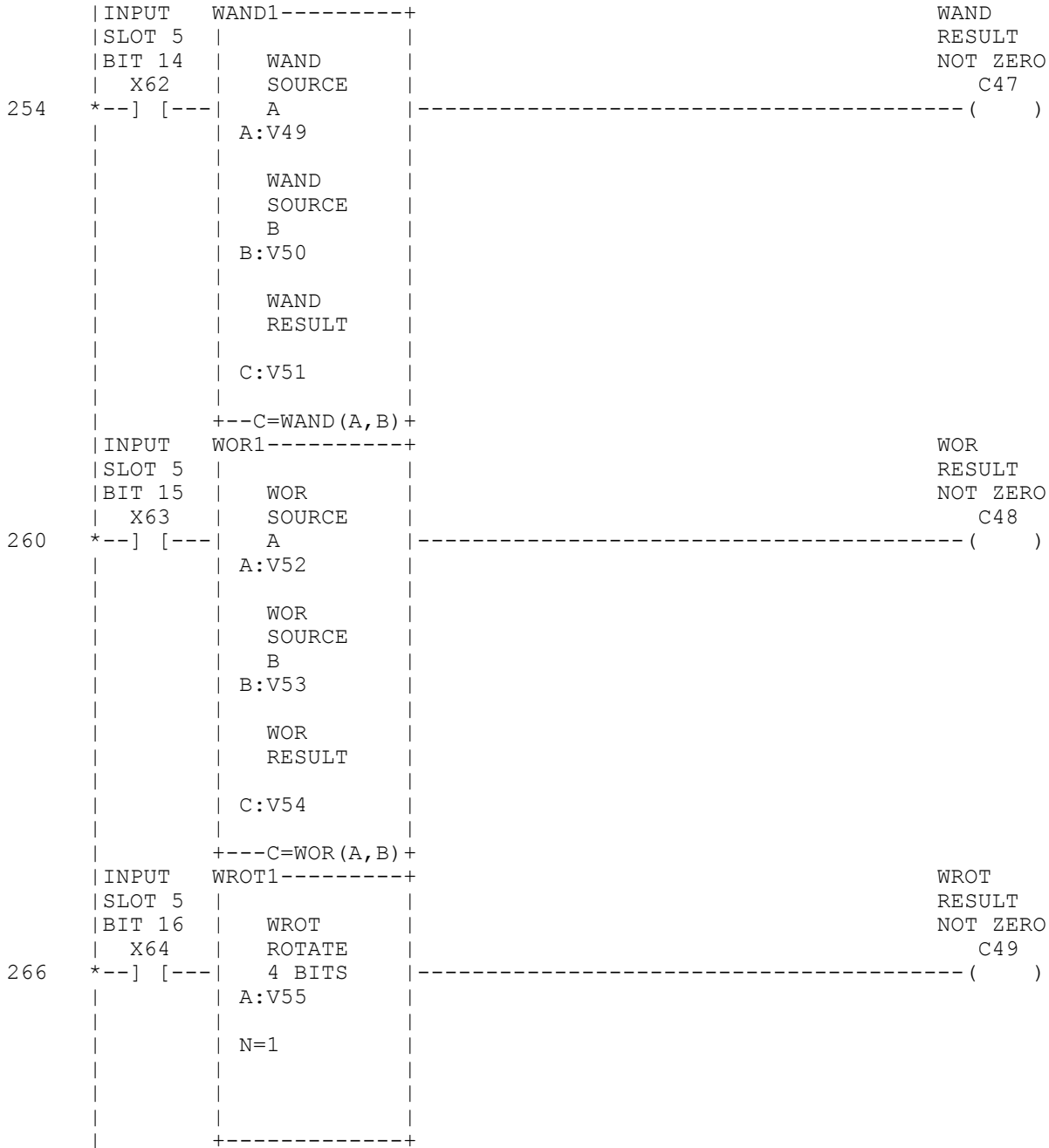
155	INPUT SLOT 4 BIT 14 X46 *--] [---	CMP2-----+ CMP A A:V3 CMP B B:V4 CMP A<B LT:C17 GT: +LT=A<B;GT=A>B+	CMP A=B C18 ()
162	INPUT SLOT 4 BIT 15 X47 *--] [---	CMP3-----+ CMP A A:V5 CMP B & A B:V6 LT: GT:C19 +LT=A<B;GT=A>B+	CMP EQUAL BIT C20 ()
169	INPUT SLOT 4 BIT 16 X48 *--] [---	CMP4-----+ CMP B & A A:V6 CMP B B:V7 CMP A<B LT:C21 GT:C22 +LT=A<B;GT=A>B+	CMP EQUAL C23 ()



	INPUT	MULT1-----+		MULT
	SLOT 5			OUTPUT
	BIT 4	MULT		
	X52	MULTIPLICND		C33
193	*--] [---		-----	()
		A:V17		
		MULT		
		MULTIPLIER		
		B:V18		
		MULT		
		PRODUCT		
		CC:V19		
		+-----CC=A*B+		
	INPUT	DIV1-----+		DIV
	SLOT 5			1=VALID
	BIT 5	MULT		0=DIV/ZERO
	X53	PRODUCT		C34
199	*--] [---		-----	()
		AA:V19		
		DIV		
		DIVISOR		
		B:V21		
		DIV		
		QUOTIENT		
		CC:V22		
		+-----CC=AA/B+		
	INPUT	SQRT1-----+		VALID
	SLOT 5			RESULT
	BIT 6	SQRT A		
	X54			C35
205	*--] [---		-----	()
		AA:V24		
		SQRT		
		RESULT		
		B:V26		
		+---B=SQRT(AA)+		

CONTERT/MOVE/LOGIC			
210	INPUT	CBD1-----+	SOURCE
	SLOT 5		OUT OF
	BIT 7	BINARY	RANGE
	X55	SOURCE	C40
*--]	[---	-----	()
	A:V30		
	BCD		
	RESULT		
	BB:V31		
	-----+		
215	INPUT	CDB2-----+	VALID
	SLOT 5		SOURCE
	BIT 8	BCD	
	X56	SOURCE	C41
*--]	[---	-----	()
	A:V33		
	BINARY		
	RESULT		
	B:V34		
	N=1		
	-----+		
221	INPUT	LDA1-----+	LDA
	SLOT 5		VALID
	BIT 9	LDA	
	X57	SOURCE	C42
*--]	[---	-----	()
	A:V35		
	AI:		
	LDA		
	RESULT		
	BB:V36		
	BI:		
	-----+		
228	INPUT	LDC1-----+	LDC
	SLOT 5		VALID
	BIT 10	TIMER 1	
	X58	90 SEC	C43
*--]	[---	-----	()
	A:TCP1		
	N=900		
	-----+		

233	INPUT	MOVE1-----+		MOVE
	SLOT 5	WORD		VALID
	BIT 11	MOVE		
	X59	TABLE		C44
	*--] [---	SOURCE	-----	()
		TS:V37		
		MOVE		
		SOURCE		
		INDEX		
		SI:V38		
		MOVE		
		TABLE		
	DESTINATION			
	TD:V39			
	DI:V40			
	N=+5			
	+-----+			
242	INPUT	MOVW1-----+		MOVW
	SLOT 5			VALID
	BIT 12	MOVW		
	X60	SOURCE		C45
	*--] [---		-----	()
		A:V41		
		MOVW		
		DESTINATION		
		ONE WORD		
		B:V42		
		N=1		
		+-----+		
248	INPUT	MOVW2-----+		MOVW
	SLOT 5			VALID
	BIT 13	MOVW		
	X61	SOURCE		C46
	*--] [---		-----	()
		A:V43		
		MOVW		
		DESTINATION		
		MULTI WORD		
		B:V48		
		N=5		
		+-----+		



271	INPUT WROT2-----+ SLOT 5 BIT 17 WROT X65 ROTATE *--] [--- 8 BITS -----+ A:V56 N=2 +-----+	WROT RESULT NOT ZERO C50 ()
276	INPUT WROT3-----+ SLOT 5 BIT 18 WROT X66 ROTATE *--] [--- 12 BITS -----+ A:V57 N=3 +-----+	WROT RESULT NOT ZERO C51 ()
281	INPUT WXOR1-----+ SLOT 5 BIT 19 WOR X67 SOURCE *--] [--- A -----+ A:V58 WOR SOURCE B B:V59 WOR RESULT C:V60 +---C=WXOR(A,B)+ PROGRAM CONTROL	WOR RESULT NOT ZERO C52 ()
287	INPUT SLOT 5 BIT 20 X68 *--] [-----+	MCR START MCR1 ()
289	MCR ZONE C53 *--] [-----+	OUTPUT SLOT 6 BIT 16 Y104 ()

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|
| MCR
| END
|
| MCRE1
291 *-----] [-----] ( E )
|
| INPUT JMP ZONE
| SLOT 5 2 START
| BIT 21
| X69 JMP2
292 *--] [-----] ( )
|
| JMP ZONE JMP ZONE
| INPUT OUTPUT
| INSTRUCTION INSTRUCTION
| C54 Y200
294 *-----] [-----] ( )
|
| JMP ZONE
| 2 END
|
| JMPE2
296 *-----] [-----] ( E )
|
| INPUT SKP ZONE
| SLOT 5 55 START
| BIT 22
| X70 SKP55
297 *--] [-----] ( )
|
| SKP ZONE SKP ZONE
| INPUT OUTPUT
| INSTRUCTION INSTRUCTION
| C55 Y201
299 *-----] [-----] ( )
|
| SKP ZONE
| 55 END
|
| LBL55
301 *-----] [-----] ( )
|
| INPUT GO TO
| SLOT 5 SUBROUTINE
| BIT 23 1
| X71 GTS1
302 *--] [-----] ( )
|
```

	ENABLE	PGTS2-----+
	SUB 2	PGTS
		PARAM 1
305	*C150	IN1 :V270
	---] [-----	PGTS
		PARAM 2
		IN2 :V271
		PGTS
		PARAM 3
		IO3 :V272
		PGTS
		PARAM 4
		IN4 :C151
		IN5 :
		IN6 :
		IN7 :
		IN8 :
		IN9 :
		IN10:
		IN11:
		IN12:
		IN13:
		IN14:
		IN15:
		IN16:
		IN17:
		IN18:
		IN19:
		+IN20:-----+


```
|ENABLE  
|SUB 2  
| C150  
318 *--]/[-----+ PGTSZ2-----+  
| PGTSZ  
| PARAM 1  
| IN1 :V273  
| PGTSZ  
| PARAM 2  
| IN2 :V274  
| PGTSZ  
| PARAM 3  
| IO3 :V275  
| PGTSZ  
| PARAM 4  
| IN4 :C153  
| IN5 :  
| IN6 :  
| IN7 :  
| IN8 :  
| IN9 :  
| IN10:  
| IN11:  
| IN12:  
| IN13:  
| IN14:  
| IN15:  
| IN16:  
| IN17:  
| IN18:  
| IN19:  
+IN20:-----+  
END OF  
PROGRAM  
END  
331 *----- ( )  
SUBROUTINE 1  
SUBROUTINE  
ONE  
SBR1  
332 *----- ( )  
|
```

```

|      FILE (TABLE) MOVE/MISC
|
| INPUT  MIRFT1-----+
| SLOT 5 |             |             MIRFT
| BIT 24 |             |             ENABLED
|   X72 |             |             C60
334 *--] [-----| SOURCE |----- ( )
|         |             |             TS:V61
|         |             |             MIRFT
|         |             |             IMAGE
|         |             |             REGISTER
|         |             |             IR:C513
|         |             |             N=32
|         +-----+
| INPUT  MIRTT1-----+
| SLOT 5 |             |             MIRTT
| BIT 25 |             |             ENABLED
|   X73 |             |             C61
341 *--] [-----| REGISTER|----- ( )
|         |             |             IR:C545
|         |             |             MIRTT
|         |             |             TABLE
|         |             |             DESTINATION
|         |             |             TD:V63
|         |             |             N=32
|         +-----+
| INPUT  MIRW1-----+
| SLOT 5 |             |             MIRW
| BIT 26 |             |             ENABLED
|   X74 |             |             C62
348 *--] [-----| REGISTER|----- ( )
|         |             |             IR:C577
|         |             |             MIRW
|         |             |             DESTINATION
|         |             |             A:V65
|         |             |             N=12
|         +-----+
| INPUT  MWFT1-----+
| SLOT 5 |             |             MWFT
| BIT 27 |             |             DONE
|   X75 |             |             C63
355 *--] [-----| POINTER |----- ( )
|         |             |             A:V66
|         |             |             MWFT
|         |             |             DESTINATION
|         |             |             WORD
|         |             |             B:V67
|         |             |             MWFT
|         |             |             TABLE
| MWFT   |             |             S:V70
| DONE   |             |             N=10
|   C63  |             |
*--]/[-----|
|         +-----+
  
```

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| INPUT      MWTT1-----+
| SLOT 5    |           |           |
| BIT 28    | MWTT     |           |
| X76       | SOURCE   |           |
363 *--] [---| WORD     |           |
|           | A:V68    |           |
|           | MWTT     |           |
|           | TABLE   |           |
|           | POINTER  |           |
|           | B:V69    |           |
|           |           |           |
|           | MWTT     |           |
| MWTT      | TABLE   |           |
| DONE      |           |           |
|           | S:V80    |           |
| C64       |           |           |
*--]/[-----| N=10    |           |
+-----+

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| INPUT      MWI1-----+
| SLOT 5    |           |           |
| BIT 29    | MWI      |           |
371 *--] [---| SOURCE   |           |
|           | INDEX    |           |
|           | A:V90    |           |
|           |           |           |
|           | MWI      |           |
|           | DESTINATION |           |
|           | INDEX    |           |
|           | B:V91    |           |
|           |           |           |
|           | MWI      |           |
|           | COUNT    |           |
|           | INDEX    |           |
|           | N:V92    |           |
+-----+

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| INPUT      MWIR1-----+
| SLOT 5    |           |           |
| BIT 30    | MWIR     |           |
378 *--] [---| SOURCE   |           |
|           | WORD     |           |
|           | A:V93    |           |
|           | MWIR     |           |
|           | DEST IMAGE |           |
|           | REGISTER |           |
|           | IR:C81   |           |
|           | N=16     |           |
+-----+

```

	INPUT	WTOT1-----+		WTOT
	SLOT 5			ENABLED
	BIT 31	WTOT		NOT DONE
	X79	SOURCE		C70
384	*---] [-----	WORD	-----	()
		WS:V94		
		WTOT		
		DESTINATION		
		TABLE		
		TD:V95		
		WTOT		
		INDEX		
	WTOT			
	ENABLED	IN:V100		
	NOT DONE			
	C70	N=5		
	*---]/[-----			
		+-----+		
	INPUT	WTTA1-----+		WTTA
	SLOT 5			ENABLED
	BIT 32	WTTA		NOT DONE
	X80	SOURCE		C71
393	*---] [-----	WORD	-----	()
		WS:V101		
		WTTA		
		SOURCE		
		TABLE		
		TS:V105		
		WTTA		
		DESTINATION		
	WTTA	TABLE		
	ENABLED	TD:V110		
	NOT DONE			
	C71	IN:V102		
	*---]/[-----	N=5		
		+-----+		
	WTTO	WTTO1-----+		WTTO
	ENABLE			ENABLED
		WTTO		NOT DONE
	C72	SOURCE		C73
403	*---] [-----	WORD	-----	()
		WS:V120		
		WTTO		
		SOURCE		
		TABLE		
		TS:V125		
		WTTO		
		DESTINATION		
	WTTO	TABLE		
	ENABLED	TD:V130		
	NOT DONE			
	C73	IN:V121		
	*---]/[-----	N=5		
		+-----+		

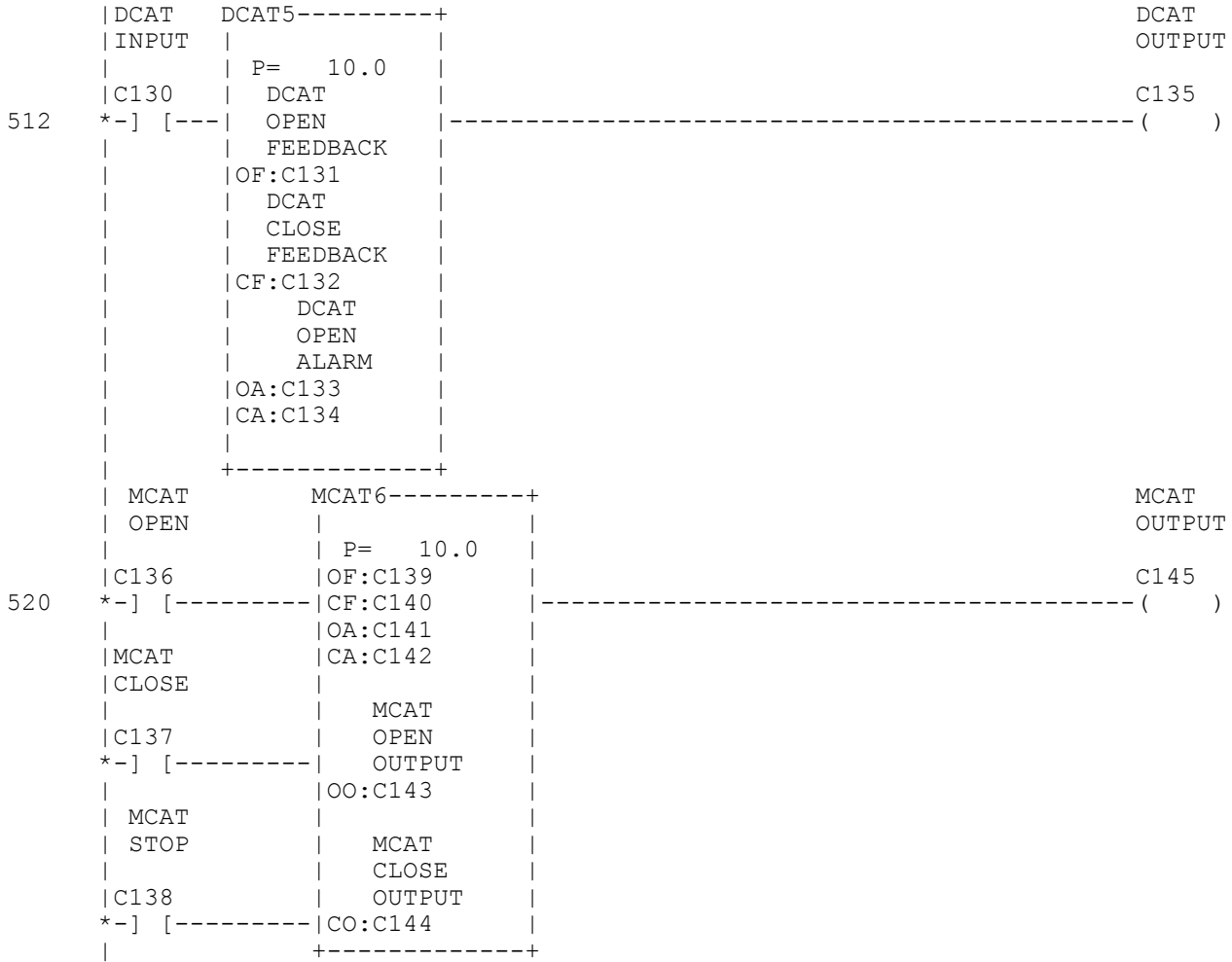
	WTTXO ENABLE	WTTXO1-----+	WTTXO ENABLED NOT DONE C75
413	*---] [-----	WTTXO SOURCE WORD WS:V140 WTTXO SOURCE TABLE TS:V145 WTTXO DESTINATION TABLE TD:V150 IN:V141 N=5	()
	WTTXO ENABLED NOT DONE C75	+	
	FILE (TABLE) SEARCH		
	SHRW CLOCK	SHRW1-----+	SHRW DONE ONE SHOT C92
423	*---] [-----	ANALOG IN SLOT 11 WORD 1 A:WX105	()
	SHRW ENABLE		
	*---] [-----	SHRW TABLE INDEX B:V160 N=7	
	SHRW DONE ONE SHOT C92	+	
	STFE ENABLE	STFE1-----+	STFE FOUND ONE SHOT C94
431	*---] [-----	CONSTANT 100 WS:K1 STFE SOURCE TABLE TS:V171 STFE INDEX IN:V170 N=9	()
	STFE FOUND ONE SHOT C94	+	

	STFND ENABLD	STFND1-----+ CONSTANT 0		STFND FOUND ONE SHOT C96
440	*---] [-----	WS:K2 STFND SOURCE TABLE TS:V182 STFND INDEX	-----	()
	STFND FOUND ONE SHOT C96	IN:V180 WO:V181 N=5		
	*---] / [-----	+-----+		
		FILE (TABLE) LOGIC/MOVE		
	TAND ENABLD	TAND1-----+ TAND SOURCE A TABLE T1:V190		TAND ENABLD C101
450	*---] [-----	TAND SOURCE B TABLE T2:V193	-----	()
		TAND DESTINATION TABLE TD:V196 N=3		
	TCPL ENABLD	TCPL1-----+ TCPL SOURCE TABLE TS:V200 TCPL DESTINATION TABLE TD:V203 N=3		TCPL ENABLD C103
458	*---] [-----	+-----+	-----	()

	TOR ENABLE	TOR1-----+ TOR SOURCE A TABLE T1:V210 TOR SOURCE B TABLE T2:V213 TOR DESTINATION TABLE TD:V216 N=3 +-----+	TOR ENABLED C105 ()
465	*--] [---		
	C104		
	TOR ENABLE	TXOR1-----+ TXOR SOURCE A TABLE T1:V220 TXOR SOURCE B TABLE T2:V223 TXOR DESTINATION TABLE TD:V226 N=3 +-----+	TXOR ENABLED C107 ()
473	*--] [---		
	C106		
	TOR ENABLE	TTOW1-----+ TTOW DESTINATION WORD WD:V231 TTOW SOURCE TABLE TS:V232 TTOW INDEX IN:V230 N=6 +-----+	TTOW ENABLED NOT DONE C111 ()
481	*--] [---		
	C110		
	TOR ENABLED NOT DONE C111 *--] / [---		

MISCELLANEOUS

	DSET ENABLE	DSET1-----+ DSET SOURCE	DSET ENABLED
490	*--] [---	DT:V250	C121 ()
	DCMP ENABLE	DCMP1-----+ DCMP SOURCE	DCMP ENABLED
495	*--] [---	DT:V255	C123 ()
	TSET ENABLE	TSET1-----+ TSET SOURCE	TSET ENABLED
500	*--] [---	TM:V260	C125 ()
	TCMP ENABLE	TCMP1-----+ TCMP SOURCE	TCMP ENABLED
505	*--] [---	TM:V265	C127 ()
		LT:	
		GT:	



```

| DRUM          +-----C-C-C-C-C-C-C-C-C-C-C-C-C-C+          DRUM
| START        | DRUM1          | 1 1 1 1 1 1 1 1 1 1 1 1 1 1 |          DONE
|              | PRESET=         1 | 7 7 7 7 7 7 6 6 6 6 6 6 6 6 |
| C160         | SEC/CNT= 0.250 | 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 |          C176
533 *--] [-----| STP CNT/STP    |----- (      )
|              | 1              2 | 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 |
|              | 2              4 | 0 0 0 0 0 0 0 0 0 0 0 0 0 1 0 |
|              | 3              2 | 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 |
|              | 4              4 | 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 |
|              | 5              2 | 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 |
|              | 6              4 | 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 |
|              | 7              2 | 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 |
| DRUM         | 8              4 | 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 |
| DONE        | 9              2 | 0 0 0 0 0 0 1 0 0 0 0 0 0 0 0 |
|              | 10             4 | 0 0 0 0 0 1 0 0 0 0 0 0 0 0 0 |
| C176        | 11             2 | 0 0 0 0 1 0 0 0 0 0 0 0 0 0 0 |
*--]/ [-----| 12             4 | 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 |
|              | 13             2 | 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0 |
|              | 14             4 | 0 1 0 0 0 0 0 0 0 0 0 0 0 0 0 |
|              | 15             2 | 1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 |
|              +16---- 0-----0-0-0-0-0-0-0-0-0-0-0-0-0-0-0+
  
```

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| EDRUM        +-----C-C-C-C-C-C-C-C+          EDRUM
| START        | EVENT DRUM2    |          1 1 1 1 1 1 1 1 |          OUTPUT
|              | PRESET=         1 |          8 8 8 8 8 8 8 9 |
| C180         | SEC/CNT= 1.100 |          3 4 5 6 7 8 9 0 |          C182
586 *--] [-----| STP CNT  EVENT |----- (      )
|              | 1              1 | 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 |
| EDRUM        | 2              2 | 0 0 0 0 0 0 0 0 0 0 0 0 0 1 1 |
| JOG          | 3              3 | 0 0 0 0 0 0 0 0 0 0 0 0 1 1 1 |
|              | 4              4 | 0 0 0 0 0 0 0 0 0 0 0 1 1 1 1 |
| C181         | 5 C195         5 | 0 0 0 0 0 0 0 0 0 0 0 1 1 1 1 |
*--]/ [-----| 6              1 | 0 0 0 0 0 0 0 0 0 0 1 1 1 1 1 |
|              | 7              2 | 0 0 0 0 0 0 0 0 1 1 1 1 1 1 1 |
| EDRUM        | 8              3 | 0 0 0 0 0 0 0 1 1 1 1 1 1 1 1 |
| OUTPUT       | 9              4 | 0 0 0 0 0 0 0 1 1 1 1 1 1 1 1 |
|              | 10             5 C196 | 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 |
| C182        | 11             1 | 0 0 0 0 1 1 1 1 1 1 1 1 1 1 1 |
*--]/ [-----| 12             2 | 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 |
|              | 13             3 | 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 |
|              | 14             4 | 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 |
|              | 15             5 C197 | 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 |
|              +16-- 0-----0-0-0-0-0-0-0-0-0-0-0-0-0-0-0+
  
```



```

| MDRMW      +---MDRMW4-----OUTPUT:V283-----+ MDRMW
| START      | MASK: V282      |+----- BIT NO. -----+| OUTPUT
|            | PRESET=      1 |          1 1 1 1 1 1 1|
| C230       |SEC/CNT= 0.010 |2 3 4 5 6 7 8 9 0 1 2 3 4 5 6| C232
764 *--] [-----|STP CNT  EVENT |-----|--( )
|            | 1      10      |0 0 0 1 1 1 1 1 1 1 1 1 1 1 1|
| MDRMW      | 2      10      |0 0 0 1 1 1 1 1 1 1 1 1 1 1 1|
| JOG        | 3      10      |0 0 0 1 1 1 1 1 1 1 1 1 1 1 1|
|            | 4      10      |0 0 0 1 1 1 1 1 1 1 1 1 1 1 1|
| C231       | 5      10 C233 |0 0 0 1 1 1 1 1 1 1 1 1 1 1 1|
*--] [-----| 6        0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
|            | 7        0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
| MDRMW      | 8        0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
| OUTPUT     | 9        0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
|            |10       0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
| C232       |11       0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
*--] [-----|12       0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
|            |13       0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
|            |14       0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
|            |15       0      |0 0 0 0 0 0 0 0 0 0 0 0 0 0 0|
|            |+16--  0-----|0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0+
  
```

```

| SHRB      SHRB1-----+ SHRB
| CLOCK     |                | OUTPUT
| C240      | SHRB           | C243
822 *--] [-----| IMAGE          |-----| ( )
|            | REGISTER
| SHRB      | IR:C249        |
| DATA     |                |
| C241      | N=20           |
*--] [-----|                |
| SHRB      |                |
| ENABLE    |                |
| RESET     |                |
| C242      |                |
*--] / [-----|                |
|            |+-----+
  
```

```

|            |-----+
829 *-----| TEXT1-----+
|            | The TEXT box will change into
|            | a rung comment.
|            |-----+
  
```

